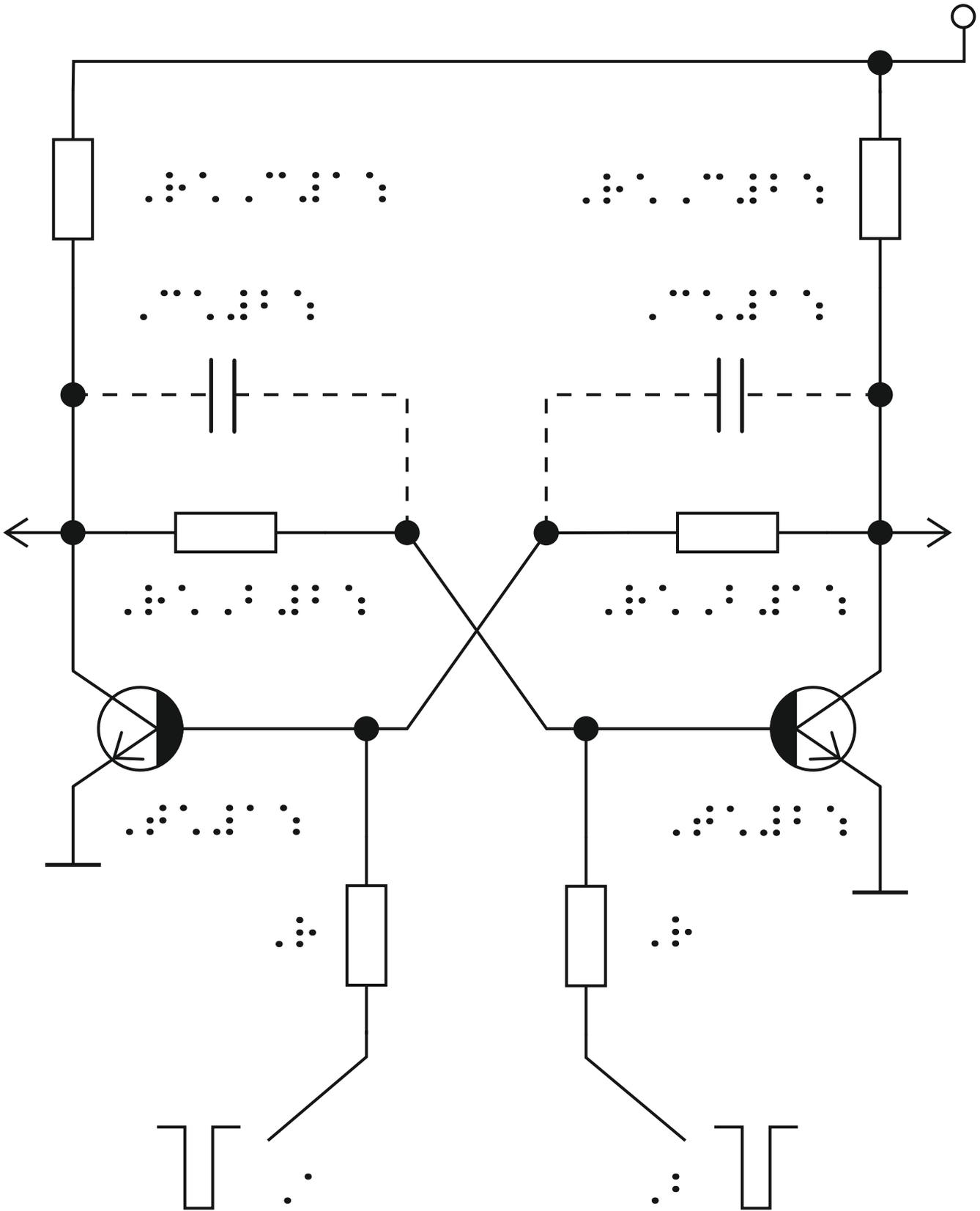


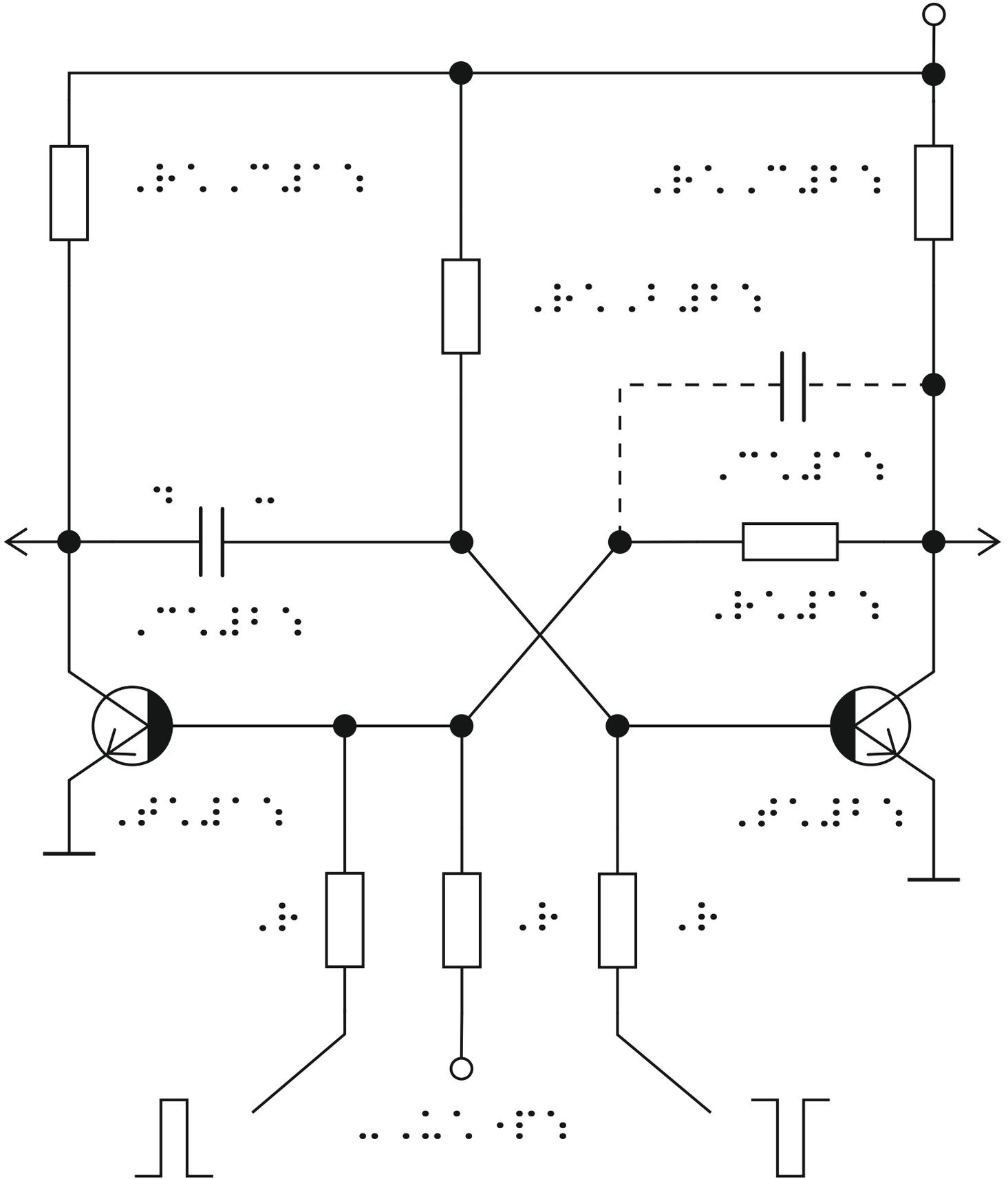
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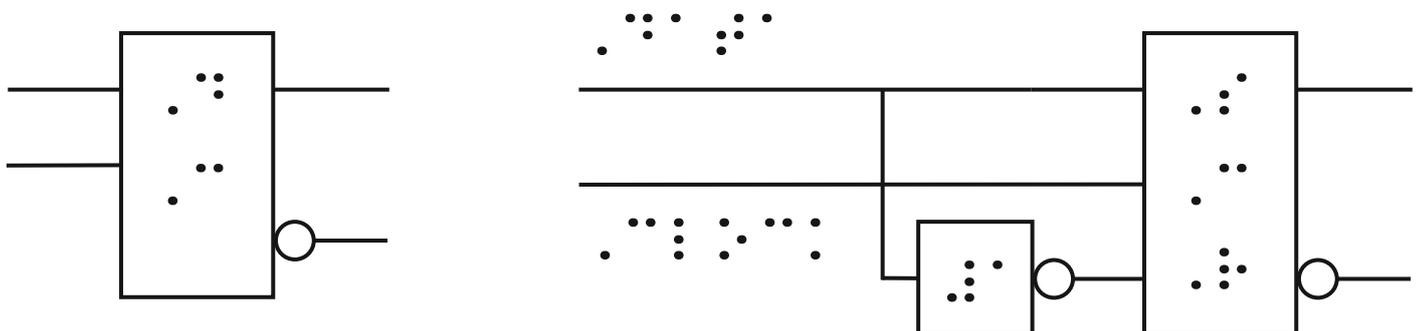
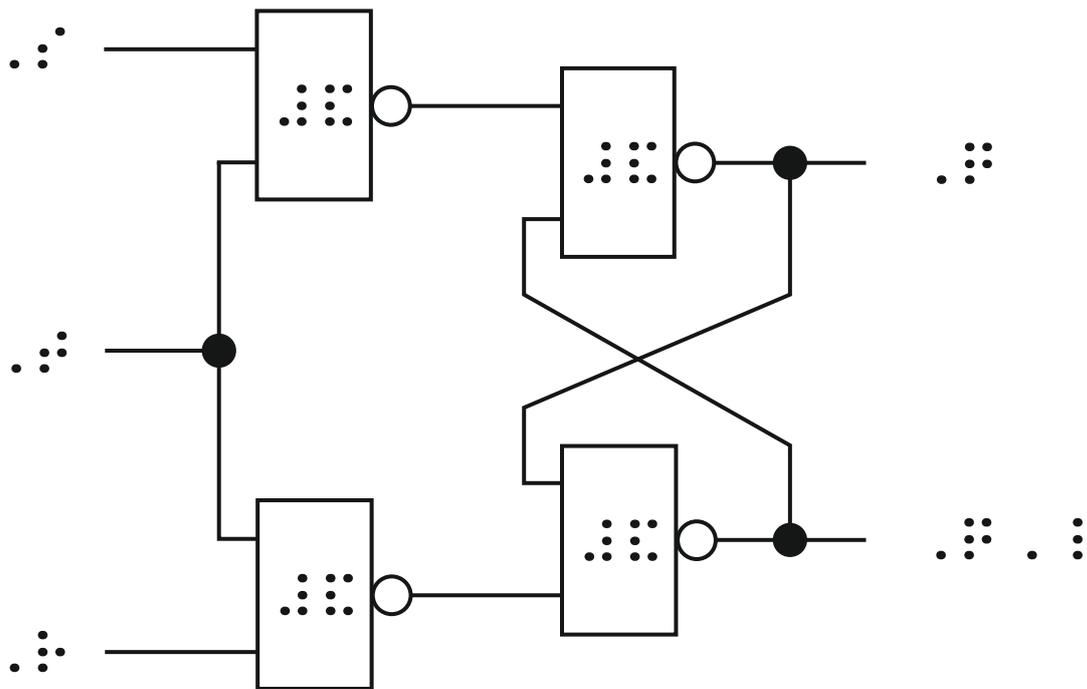
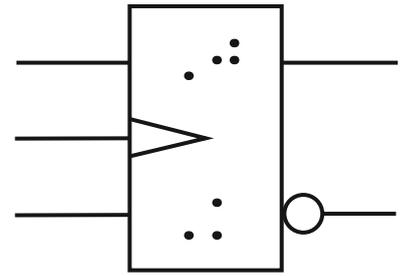
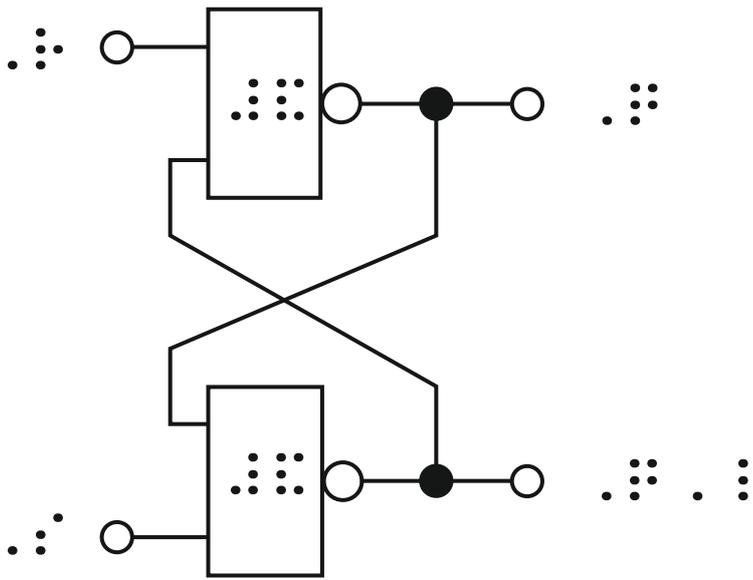
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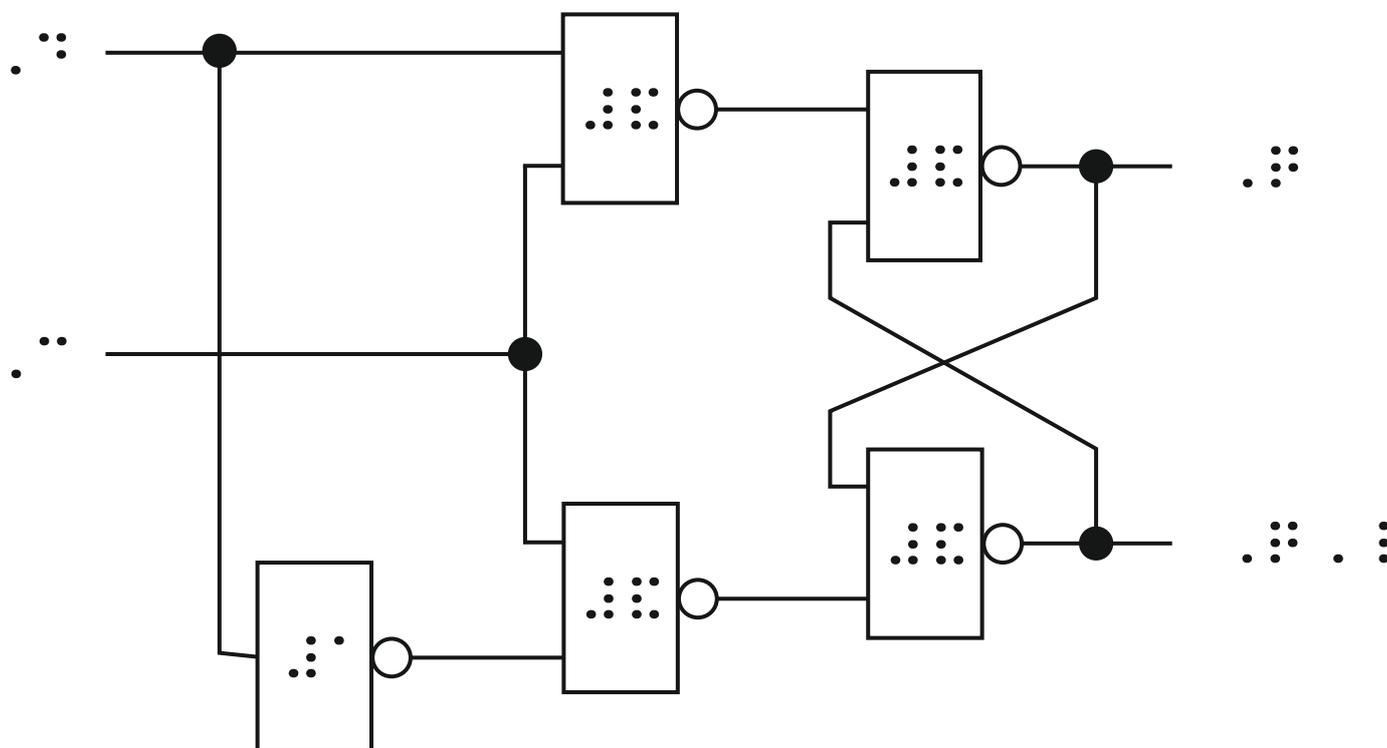
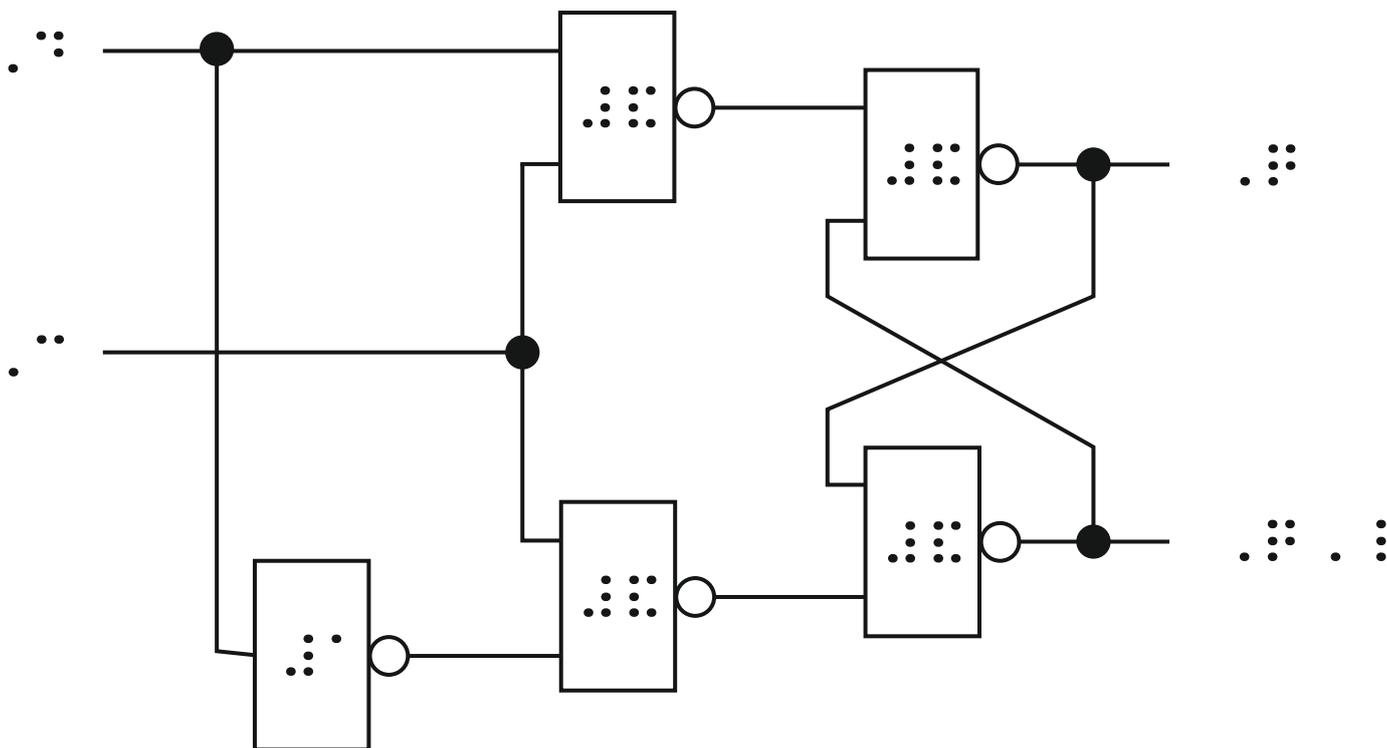


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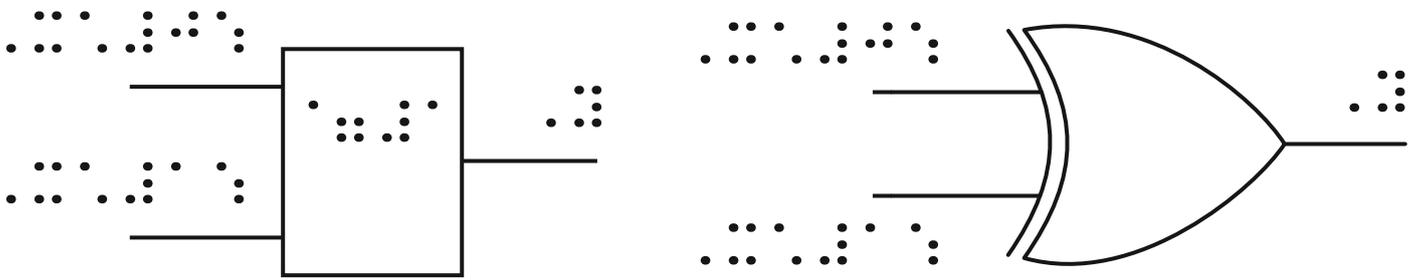
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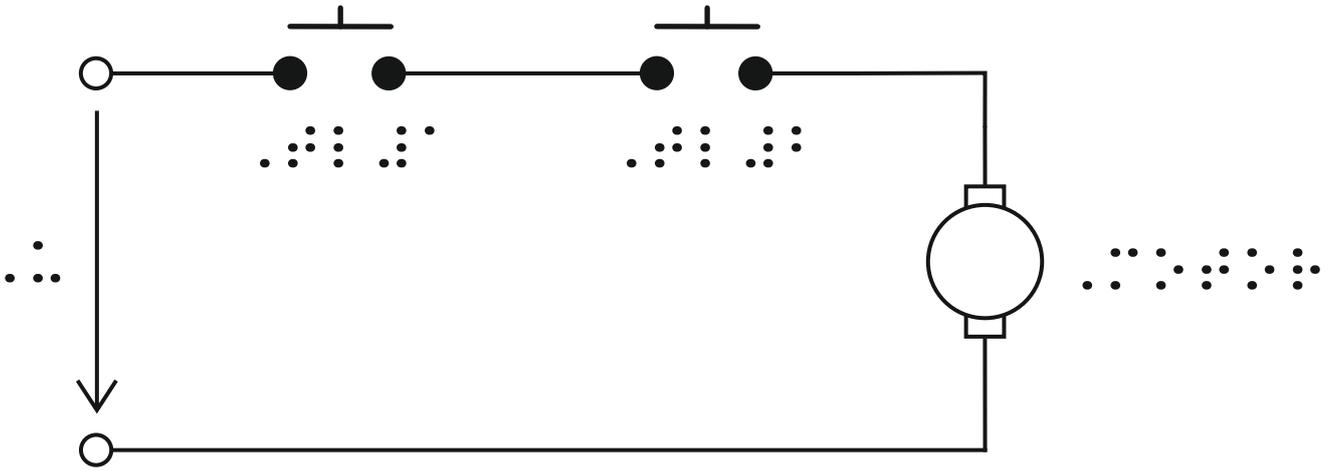




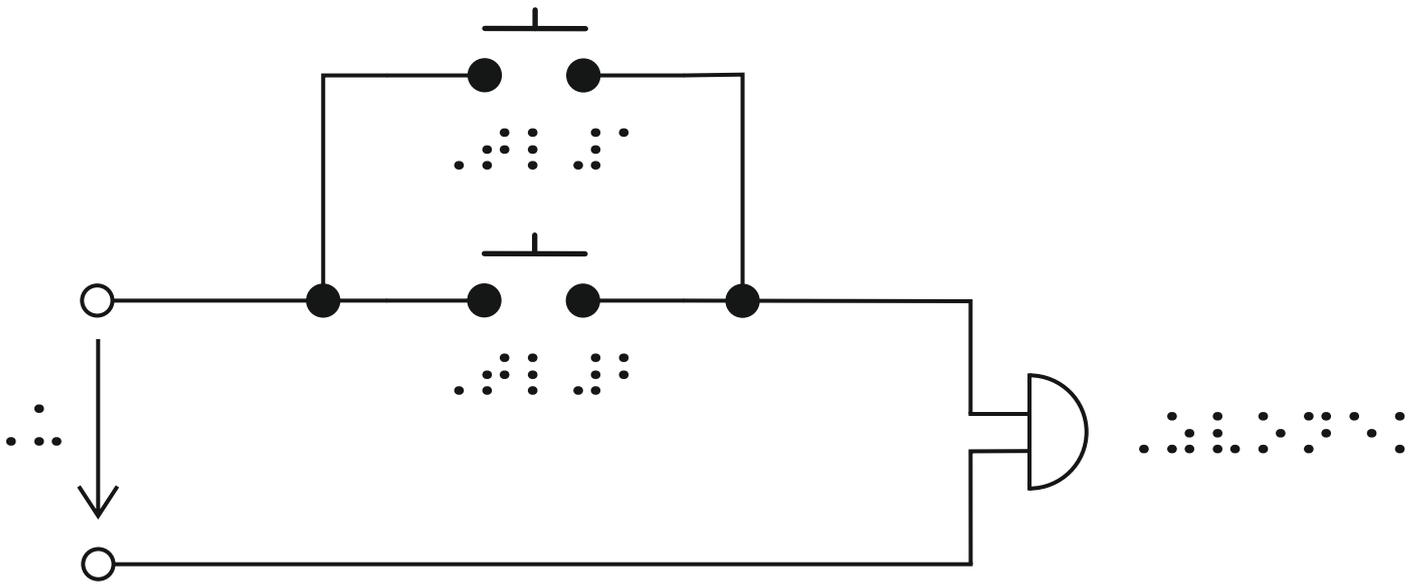
dvakrát totéž



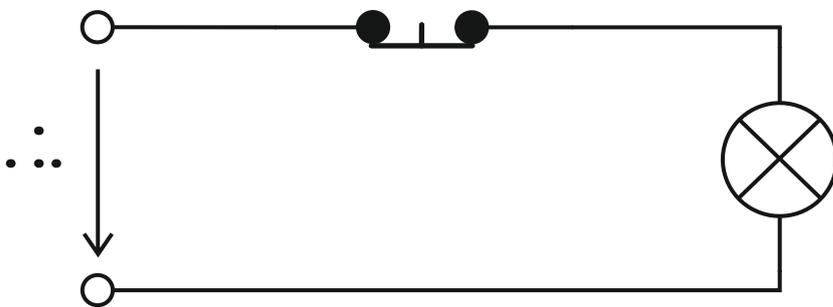
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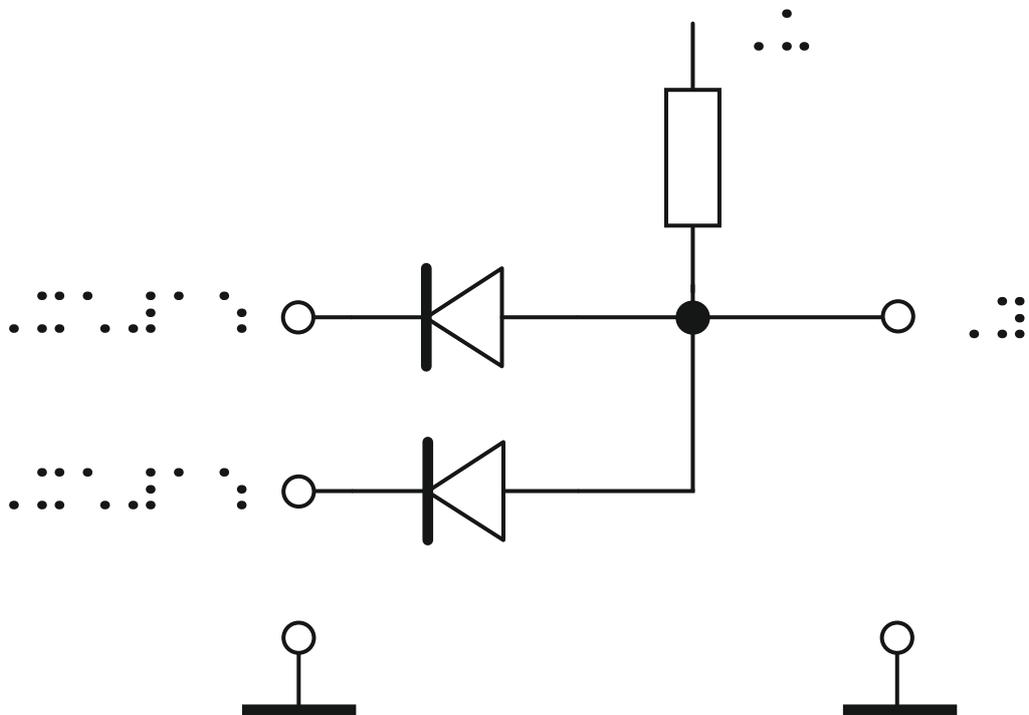
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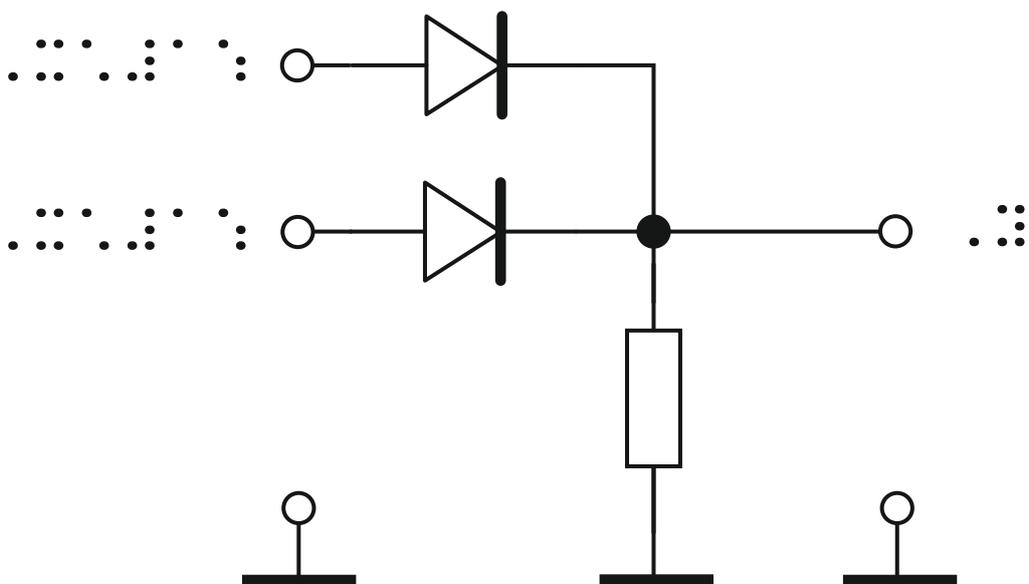
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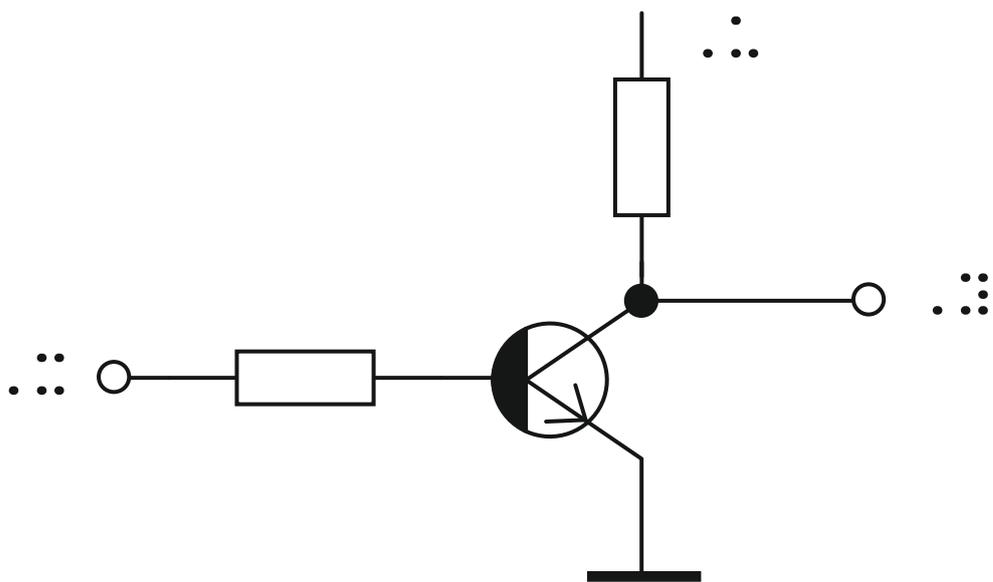
obr. 9



obr. 10

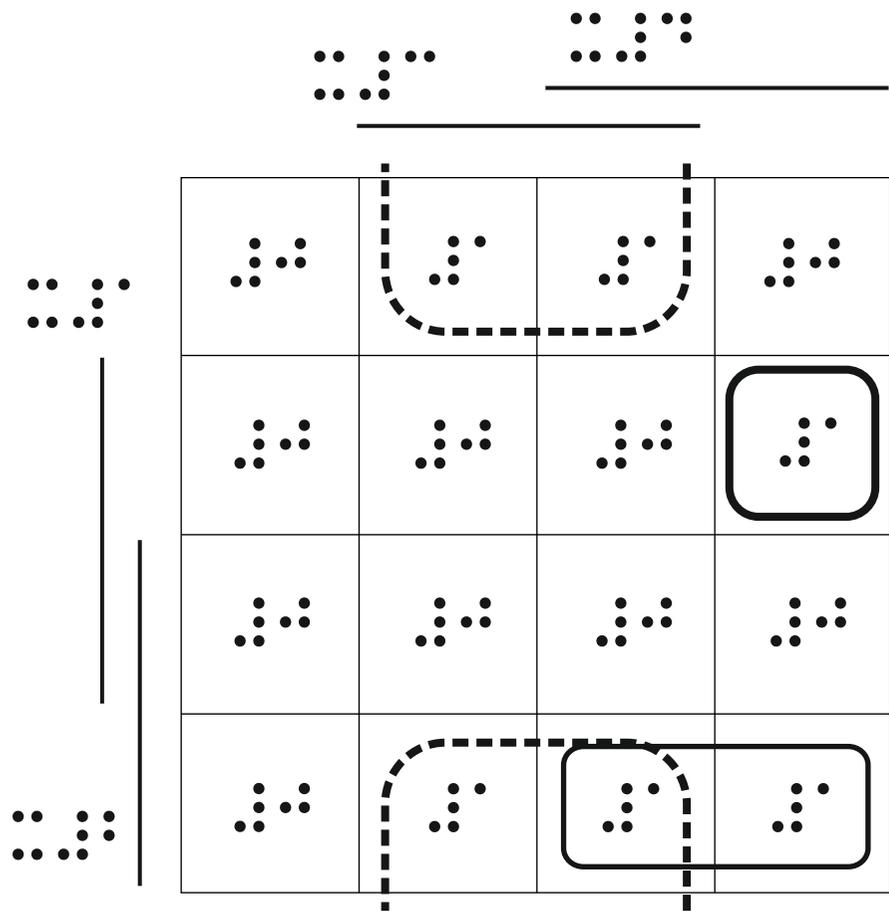


obr. 11

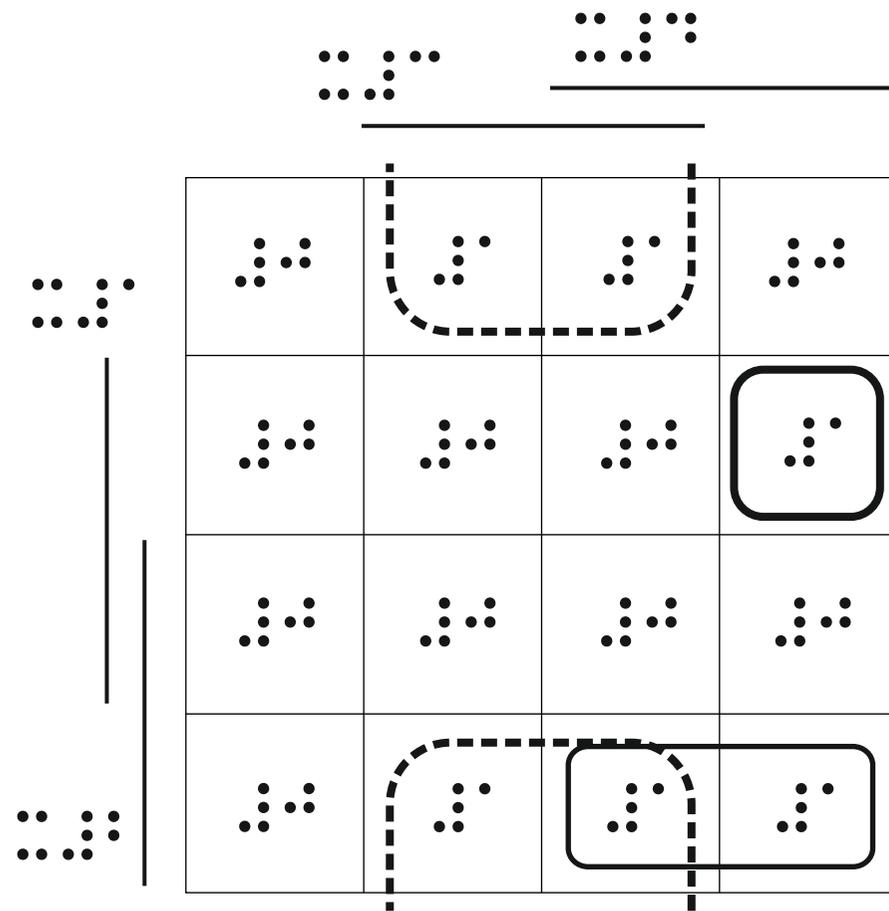


obr. 12

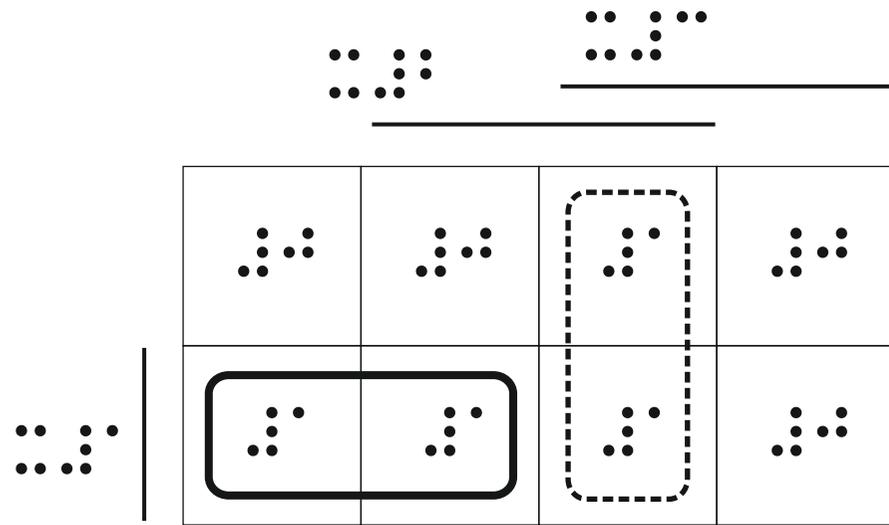
obr. 4



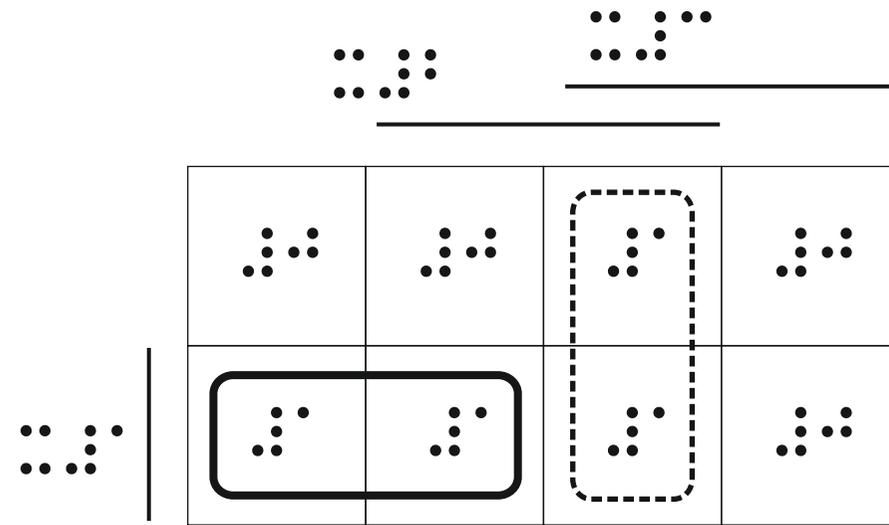
obr. 4



obr. 3

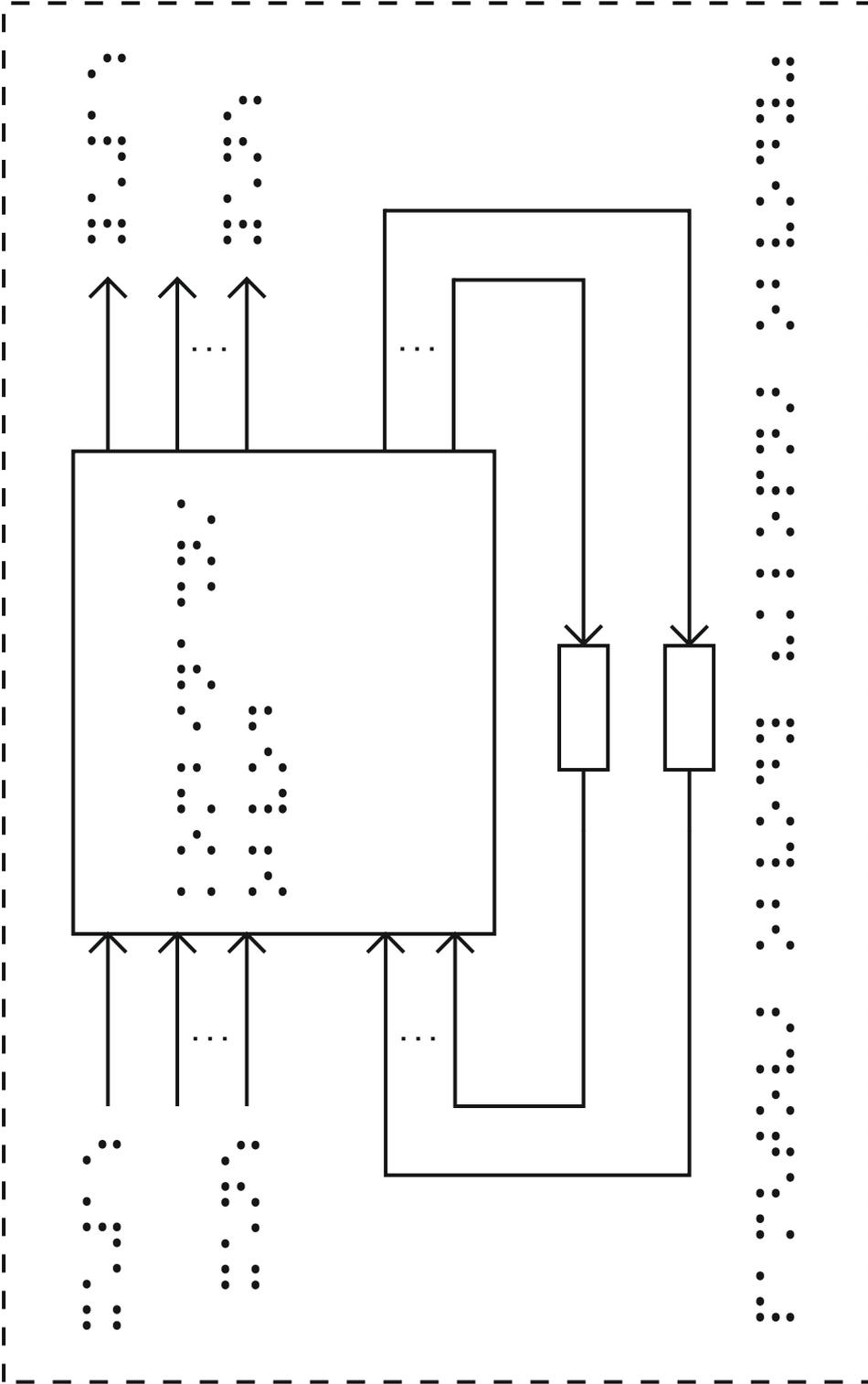


obr. 3



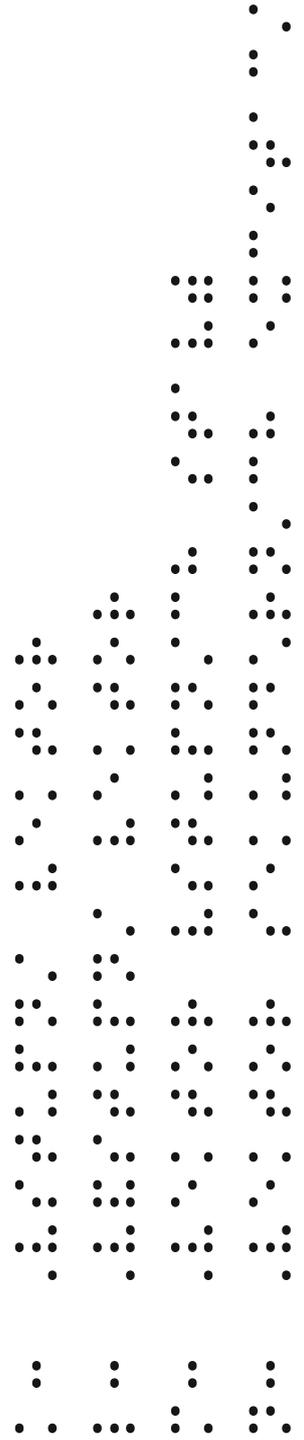
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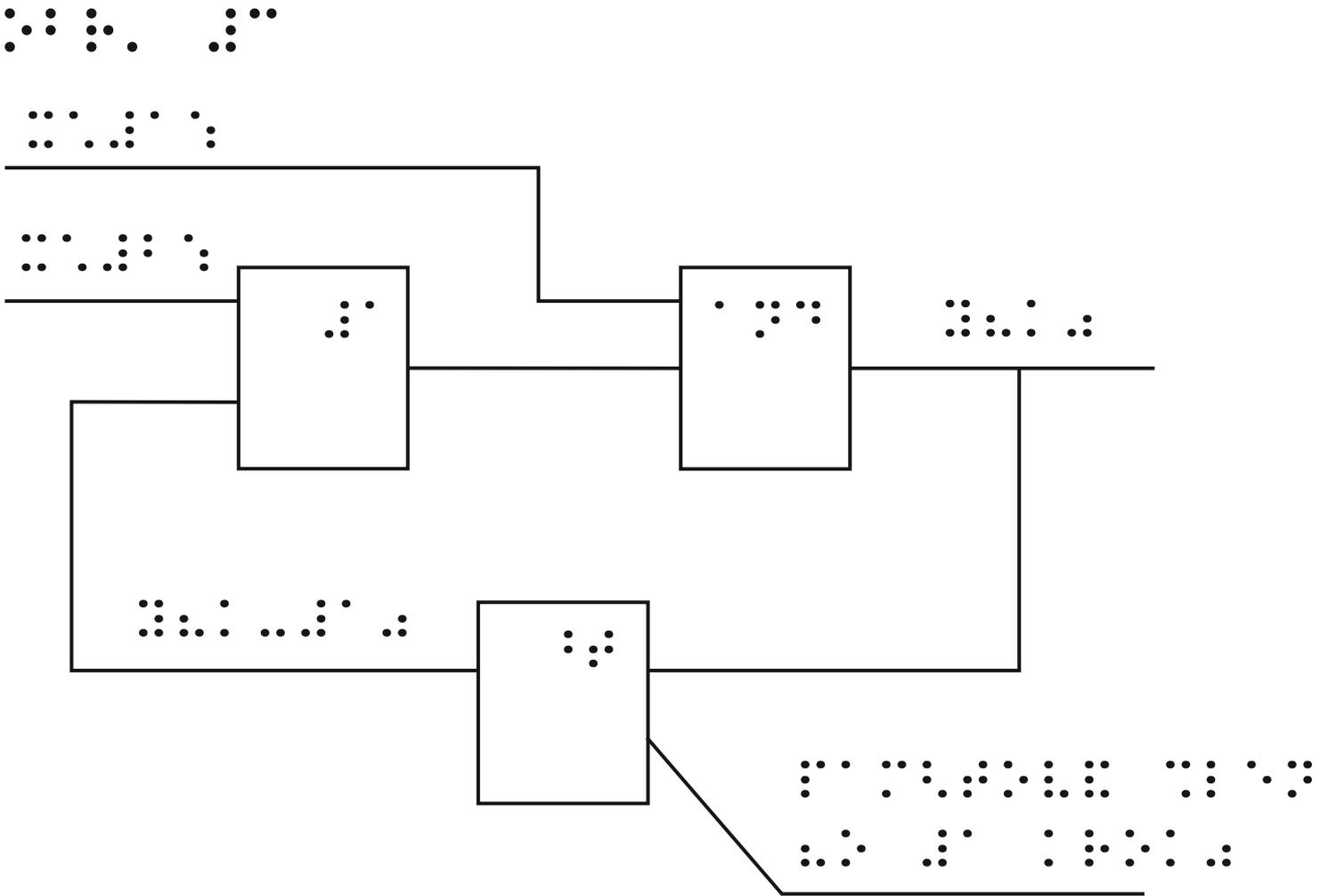
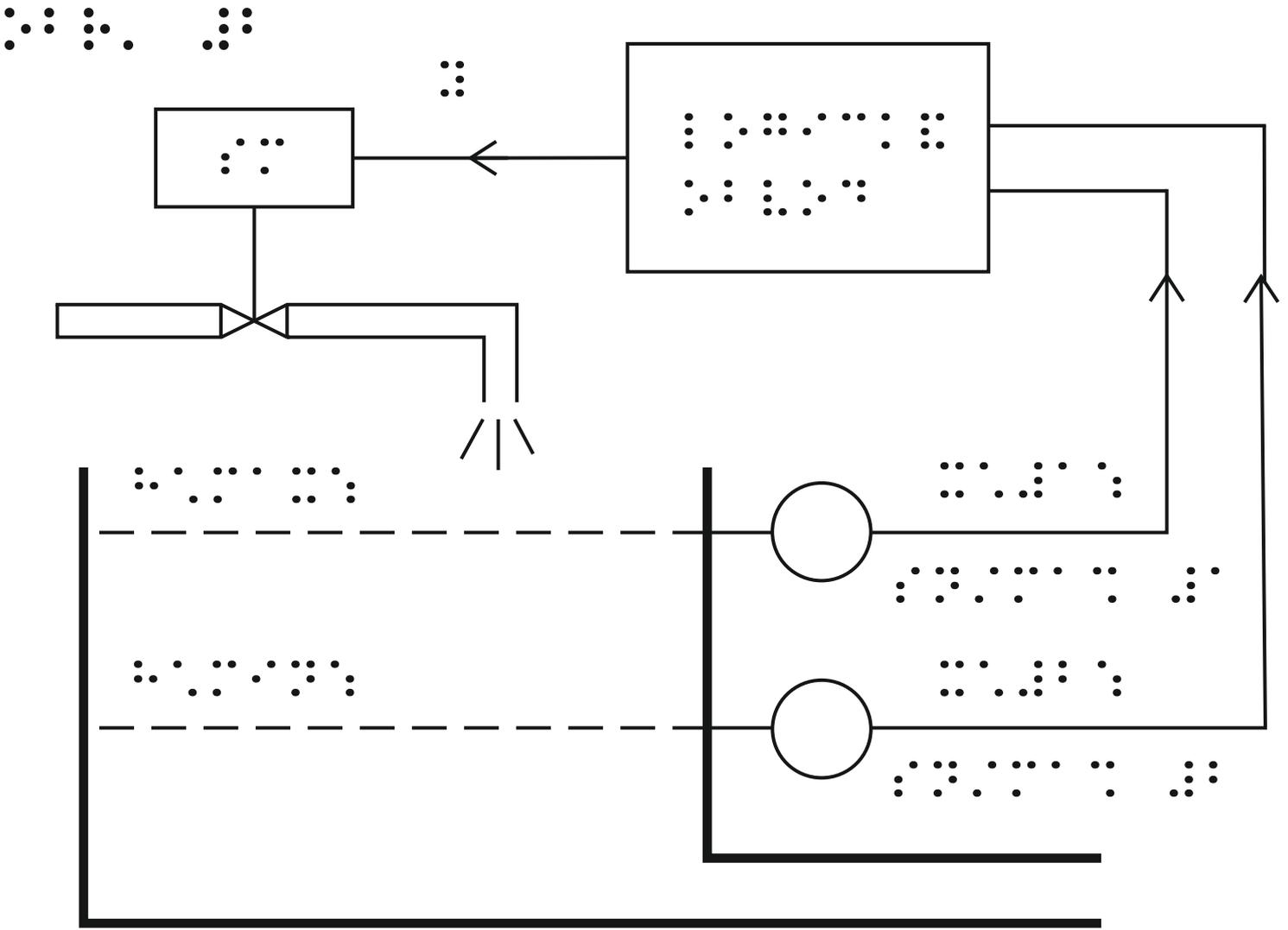
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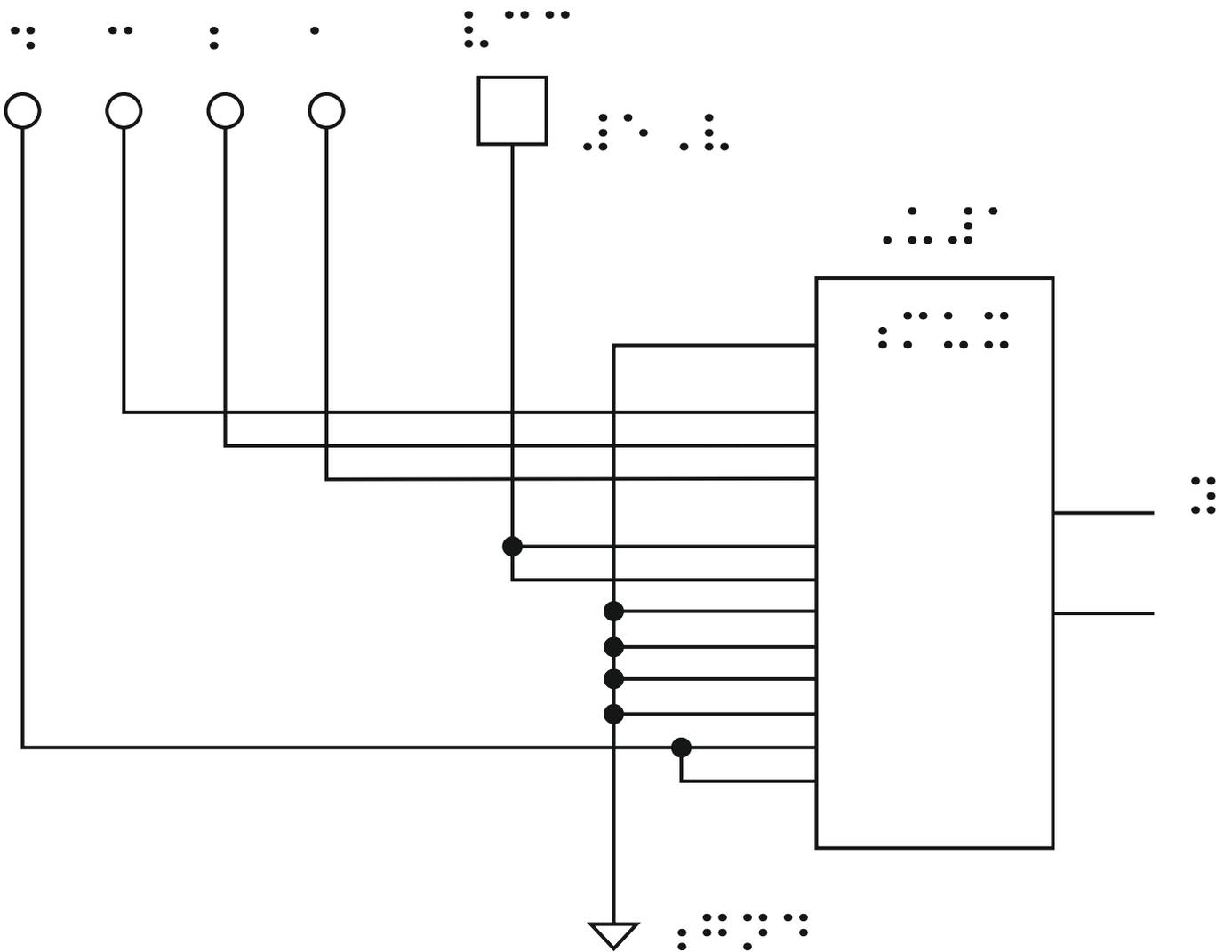
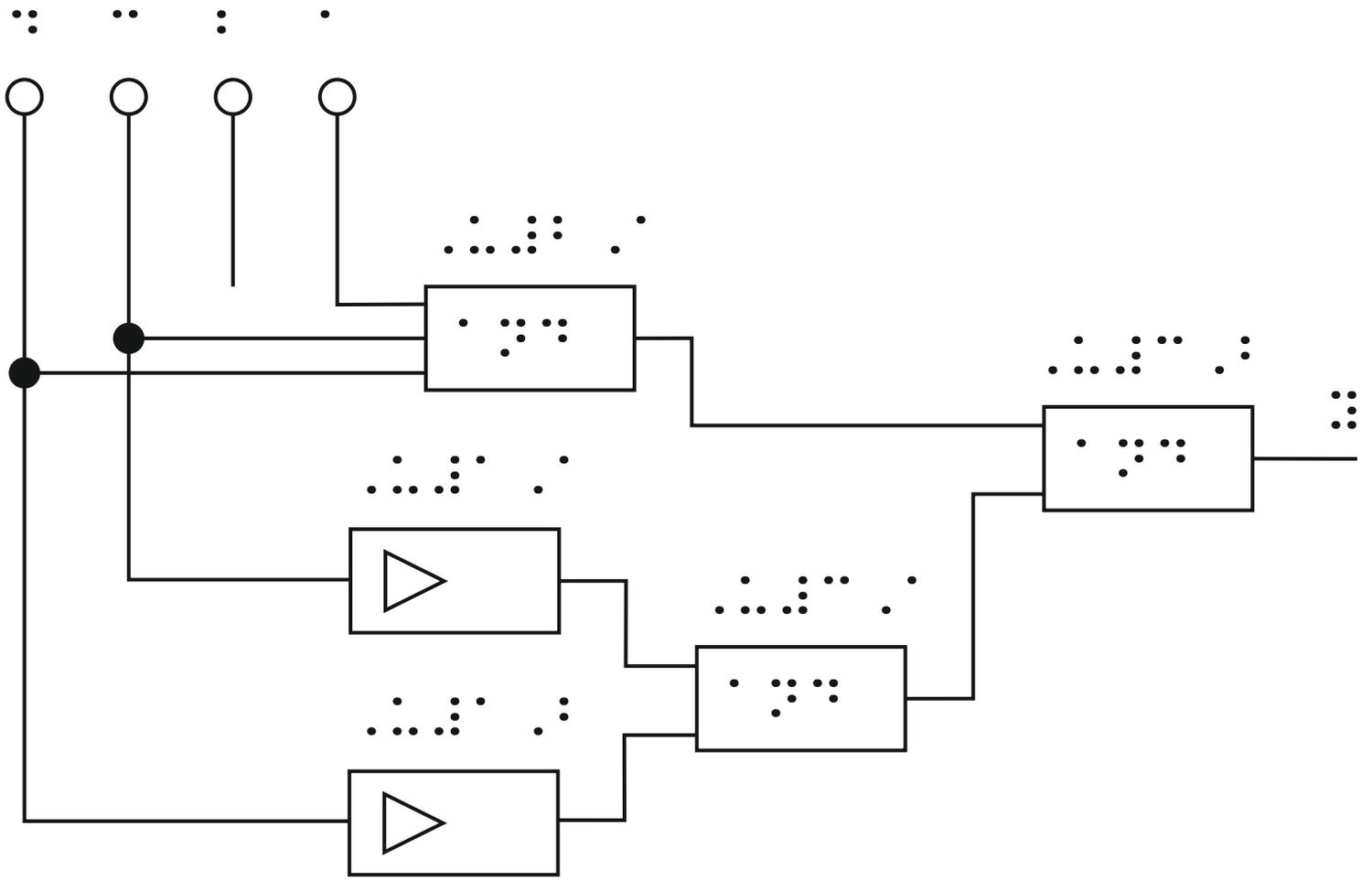
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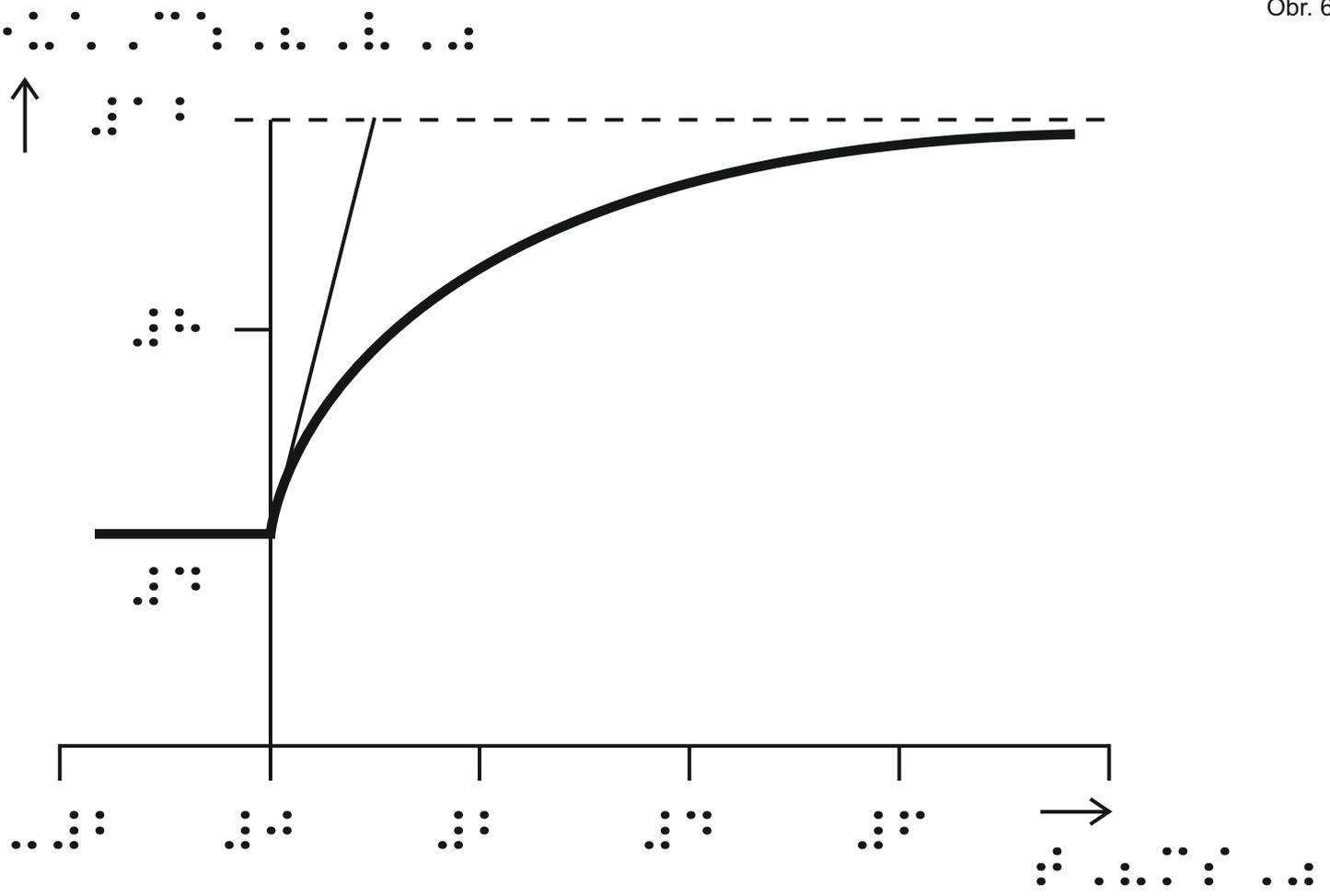
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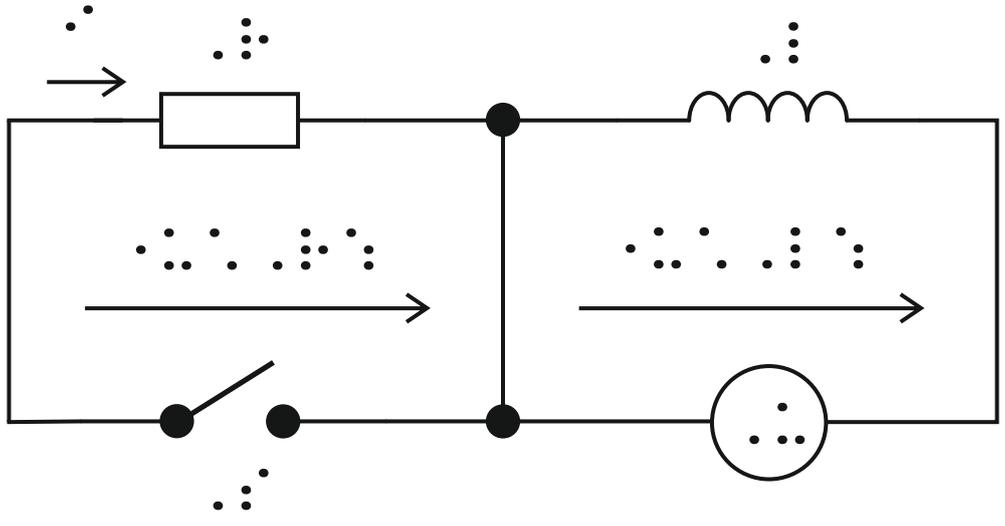


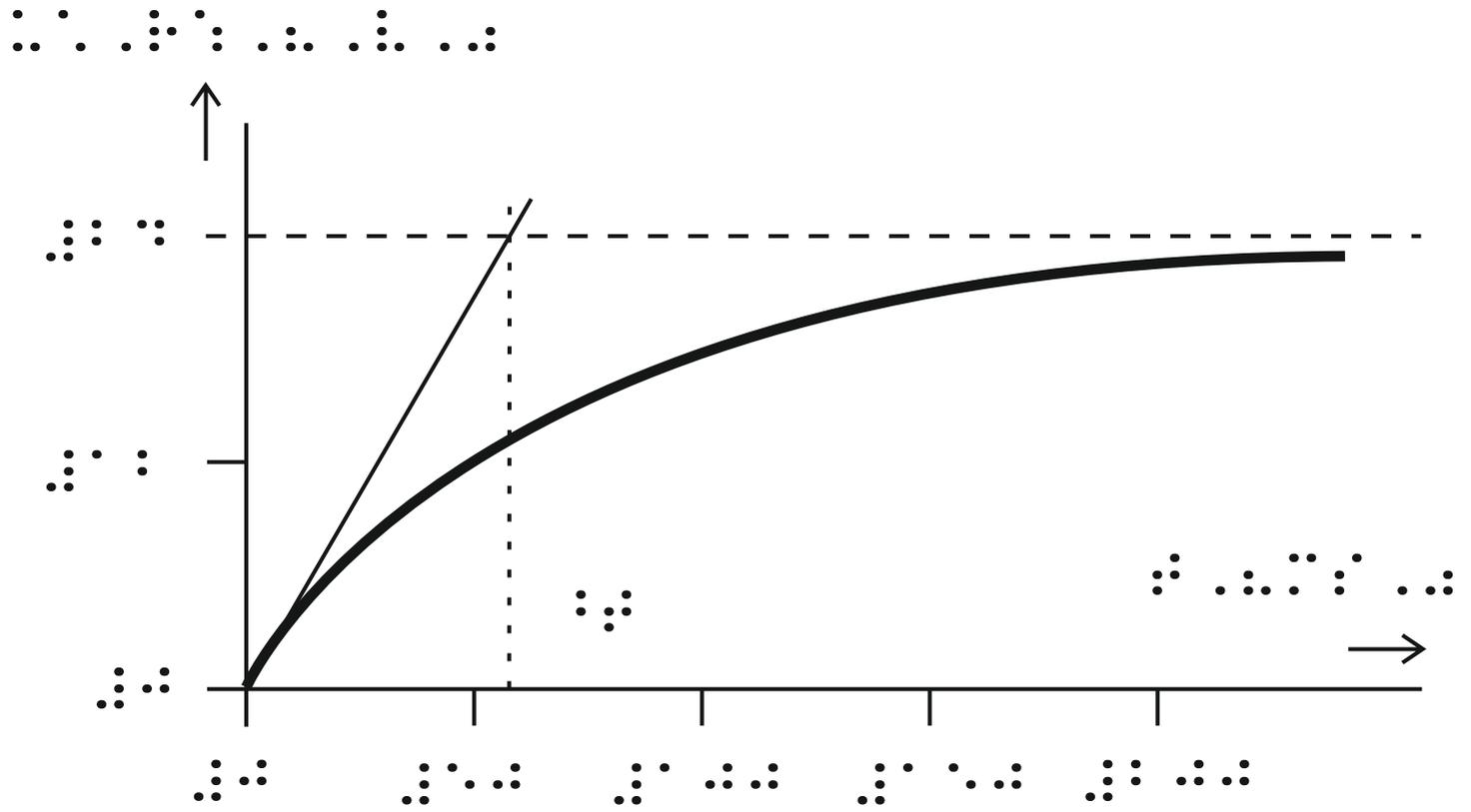
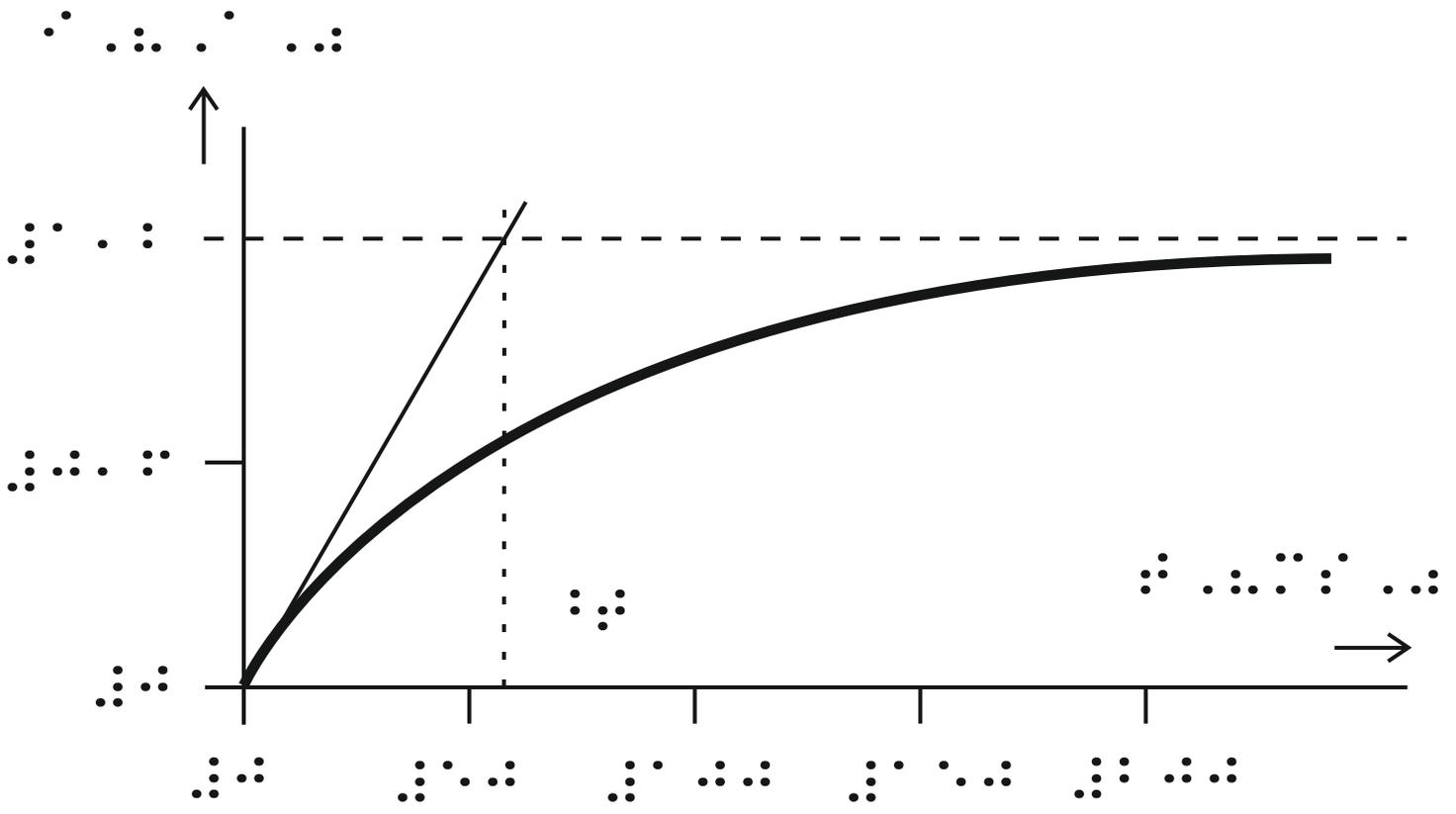


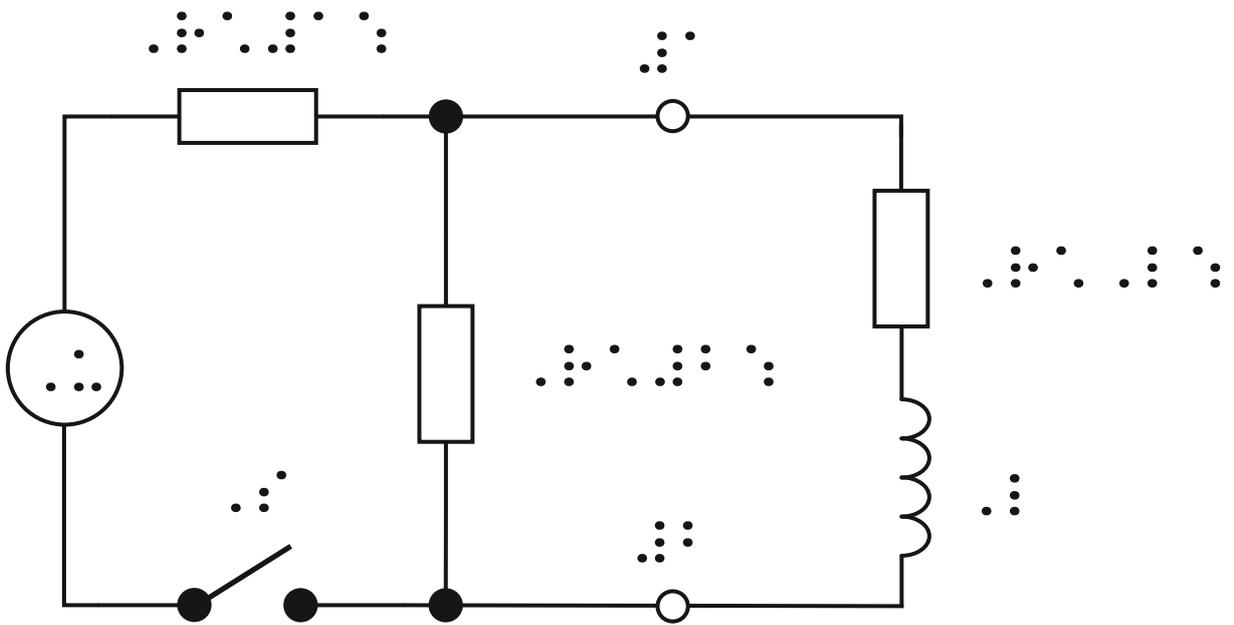
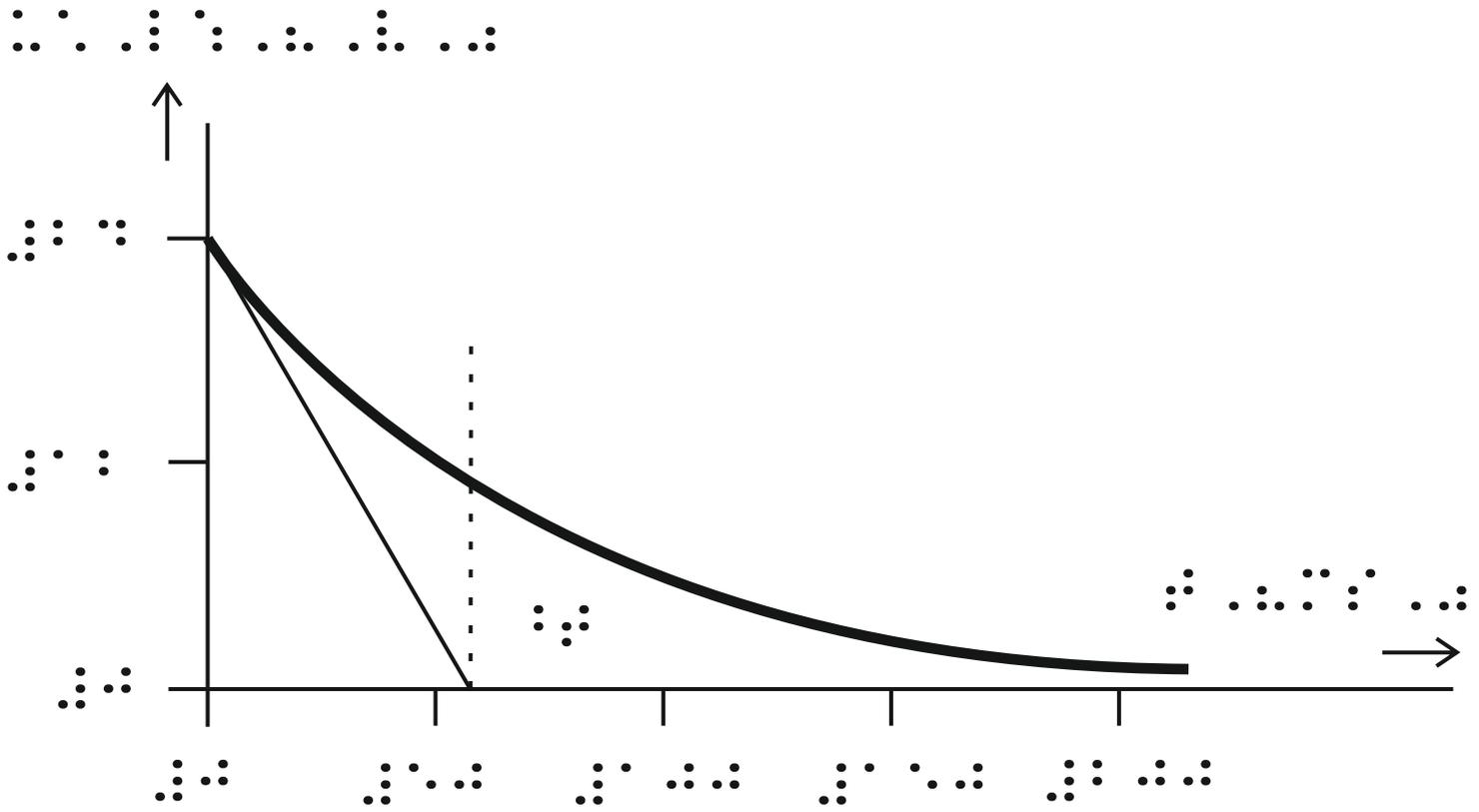
Obr. 6.4

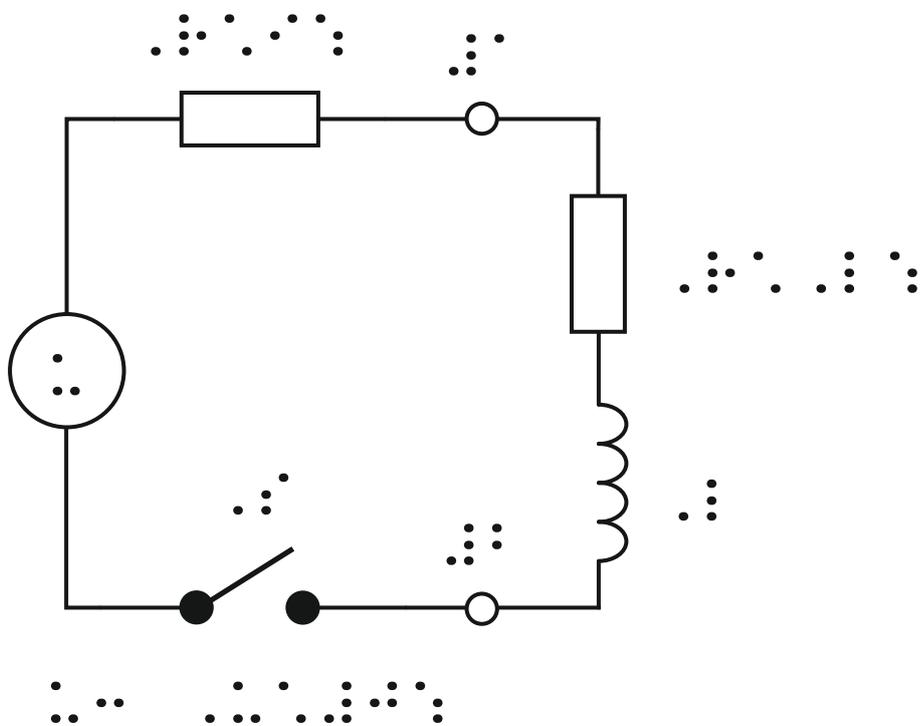


Obr. 6.5

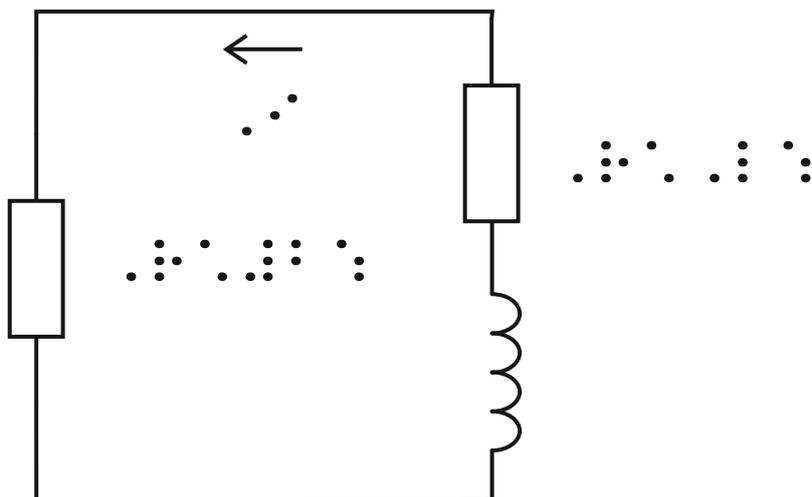




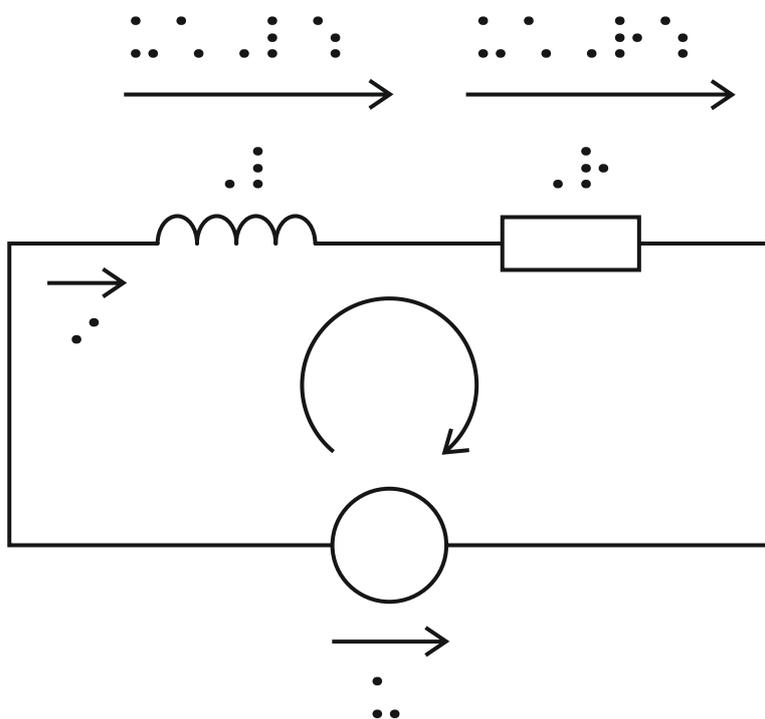


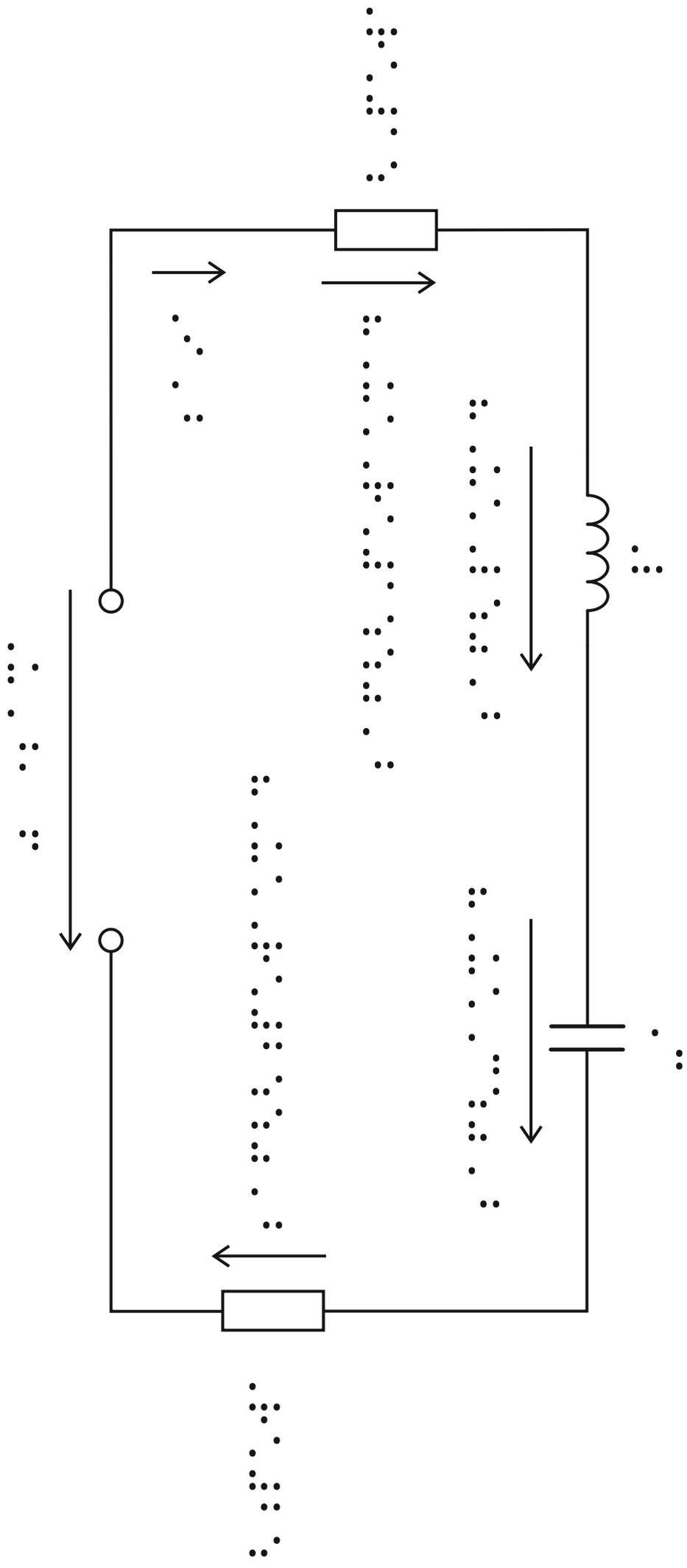


Obr. 7.3

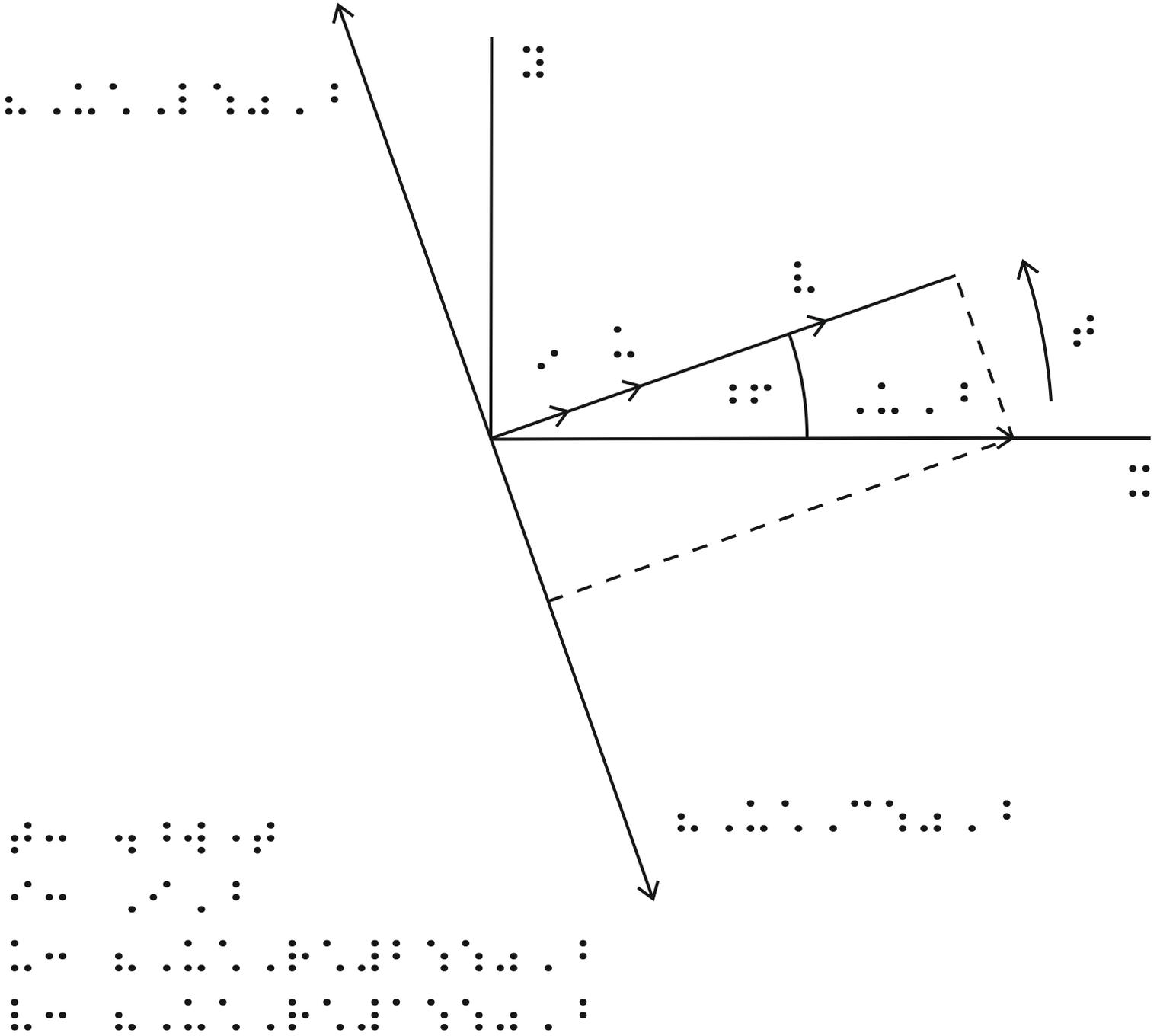


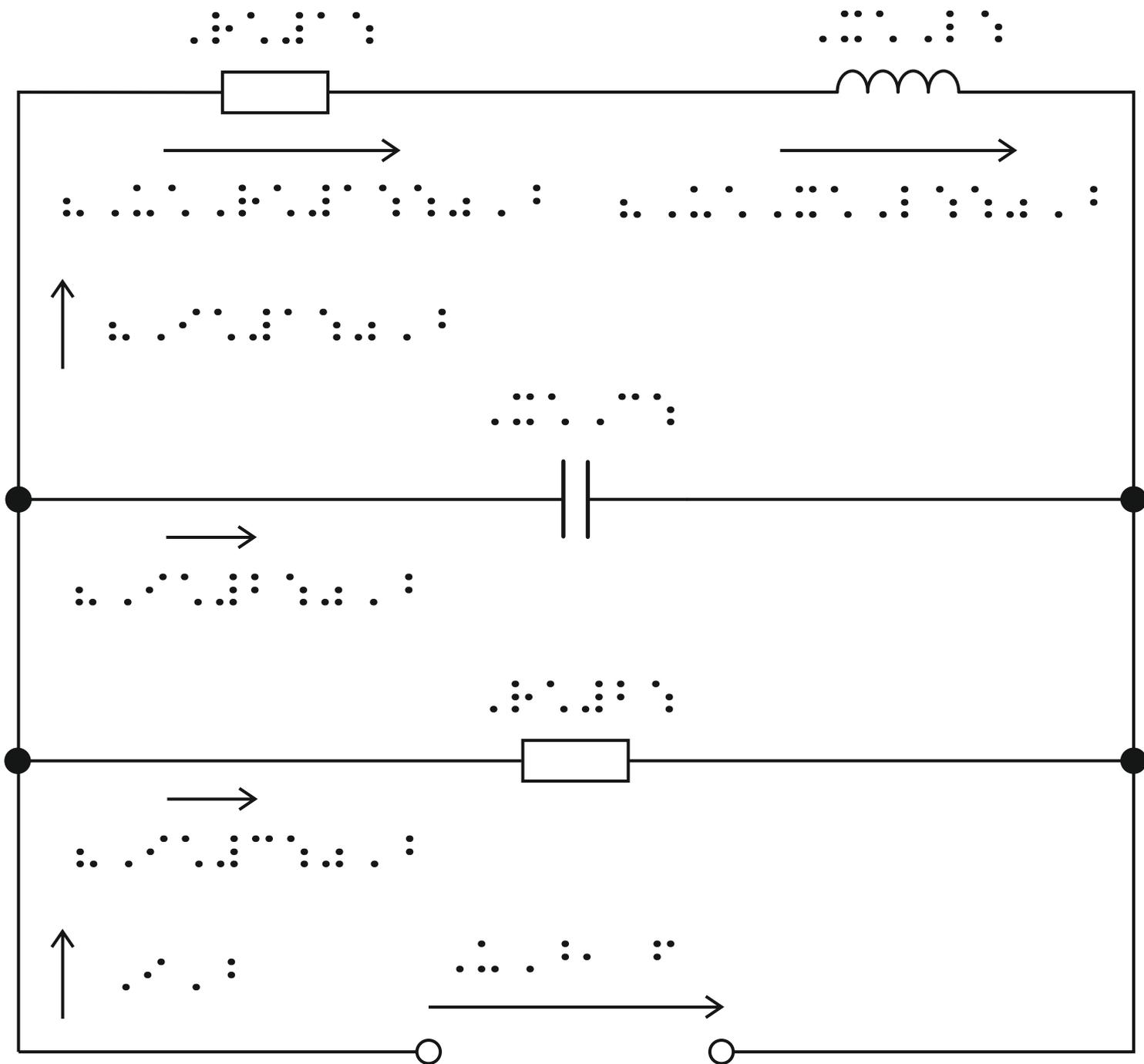
Obr. 7.4

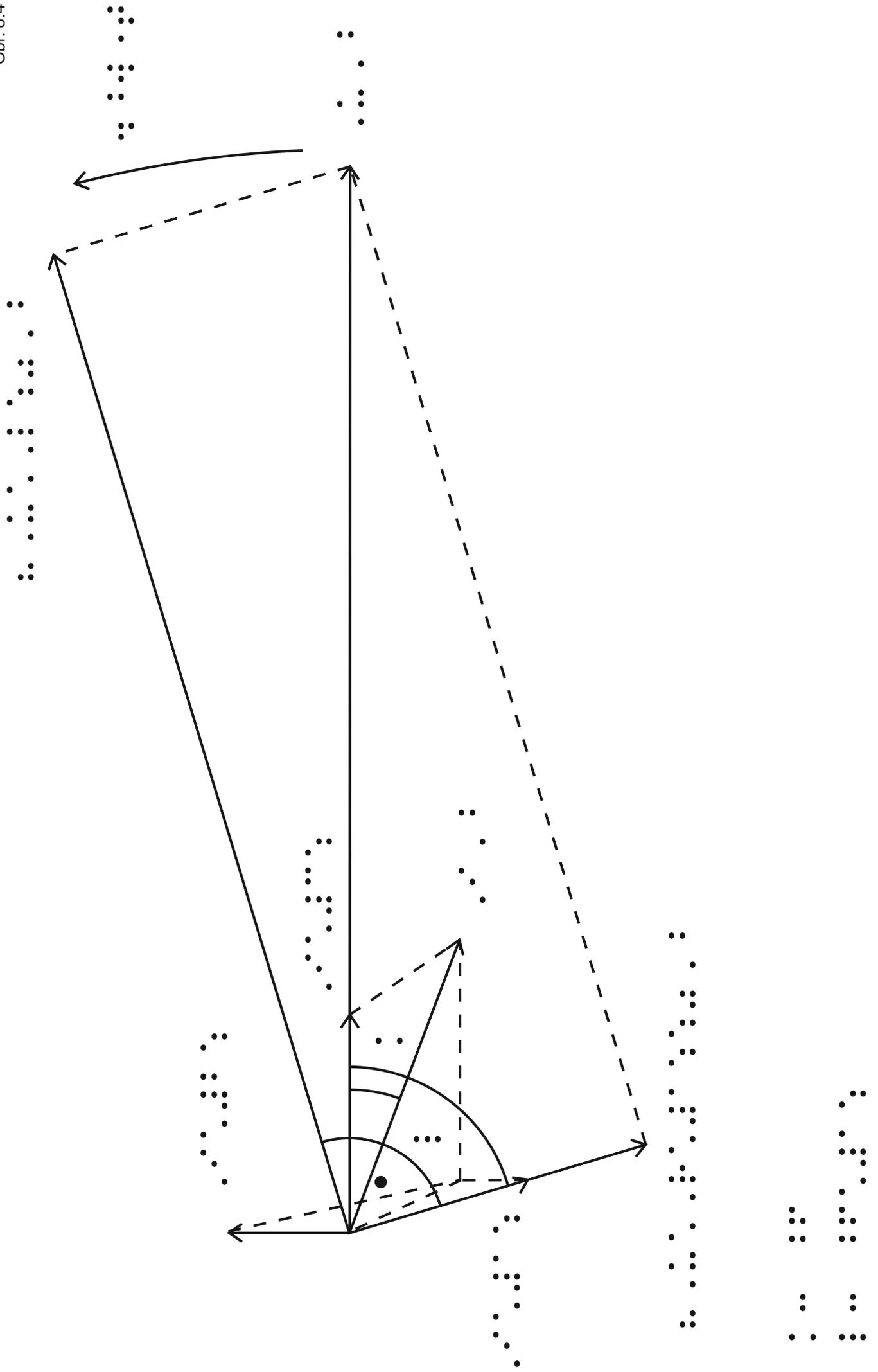


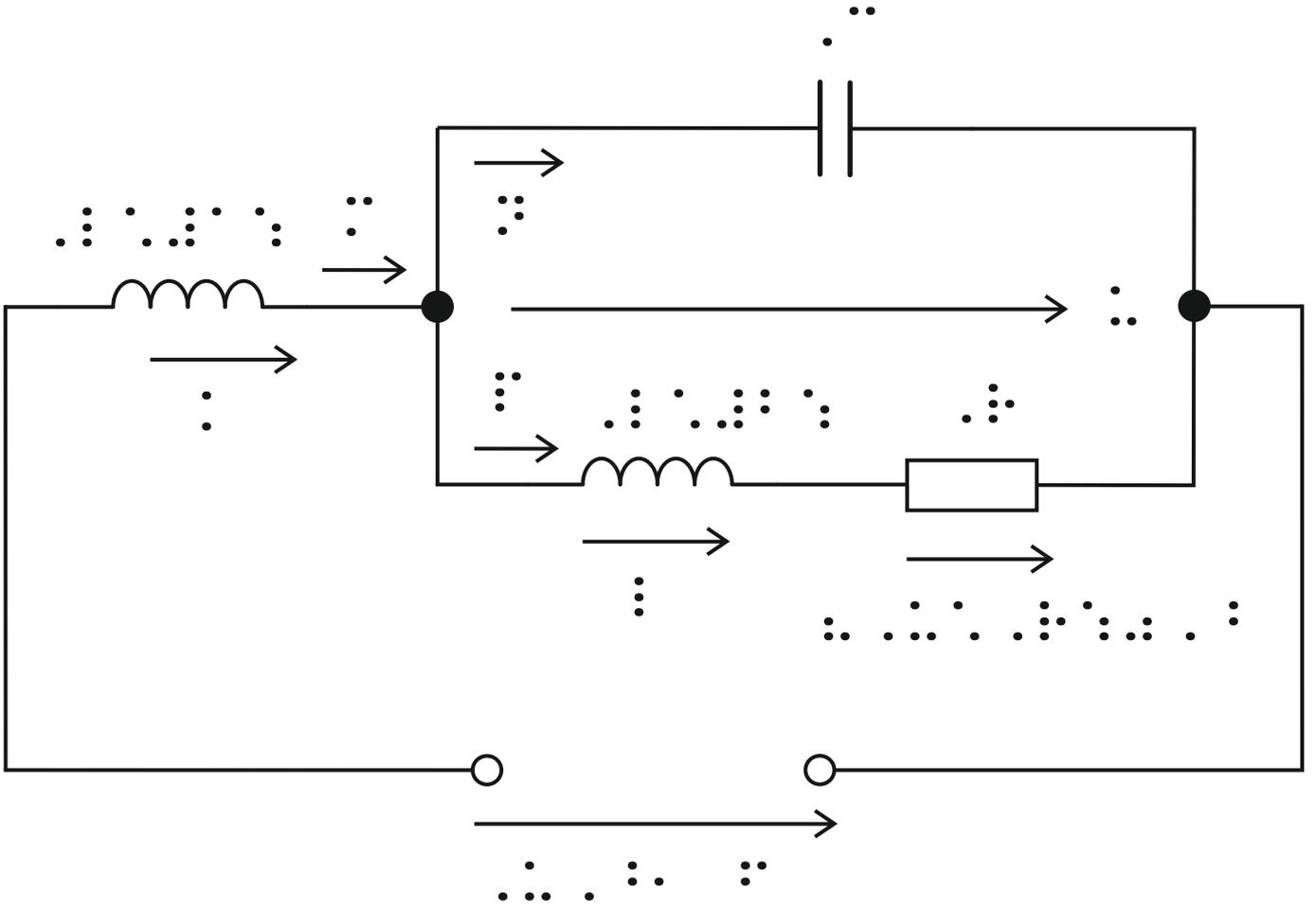


Obt: 8.1

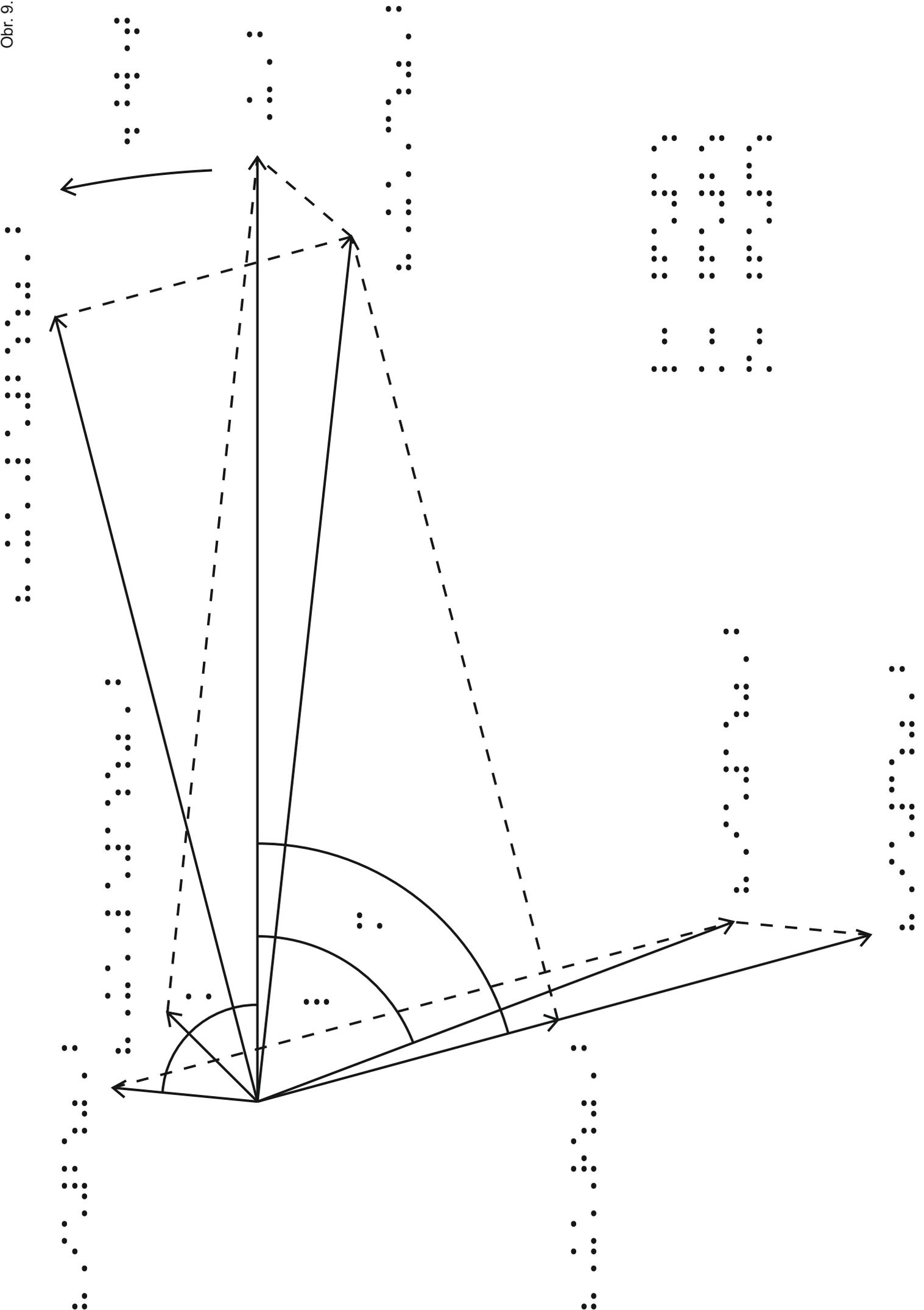


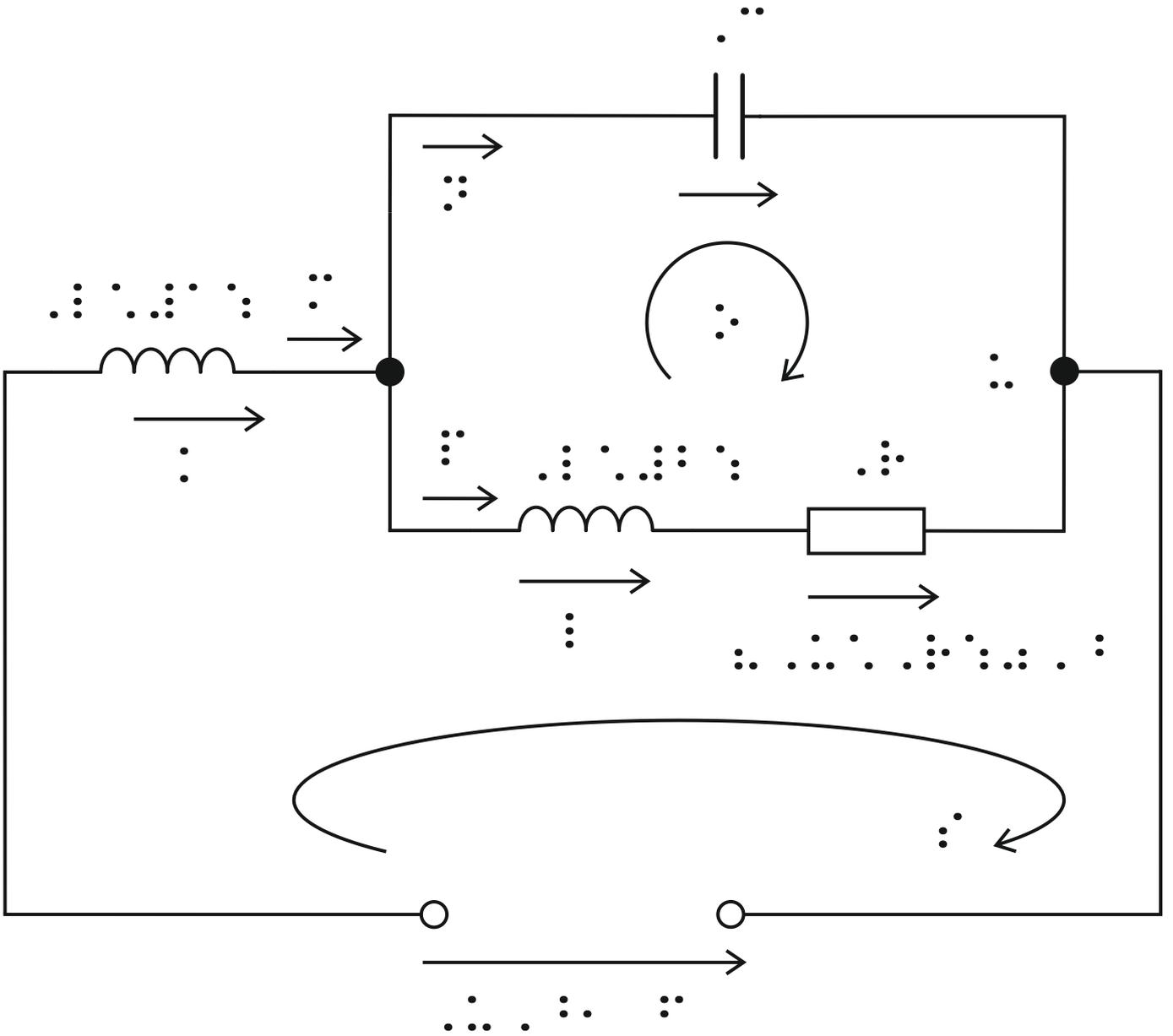




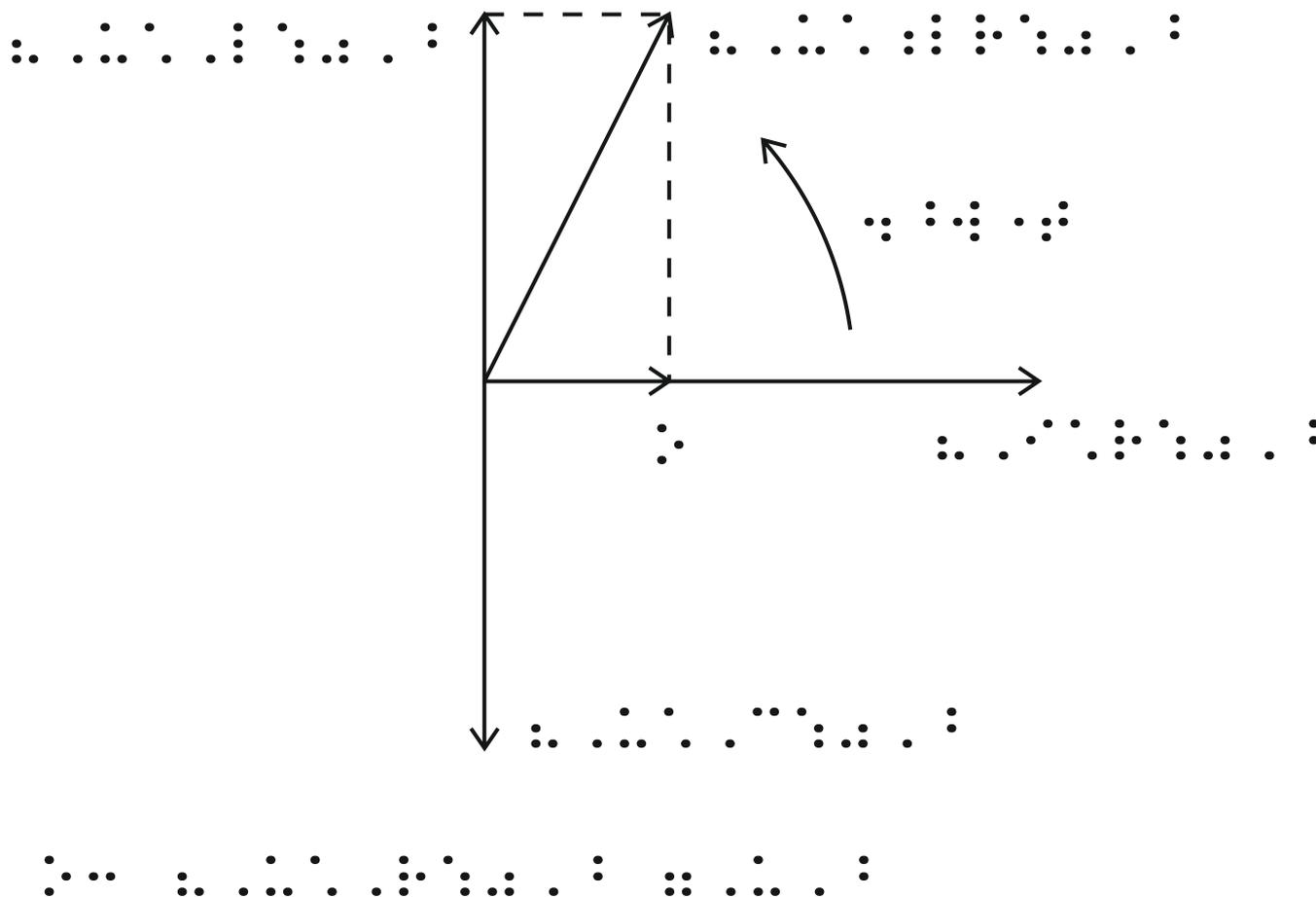
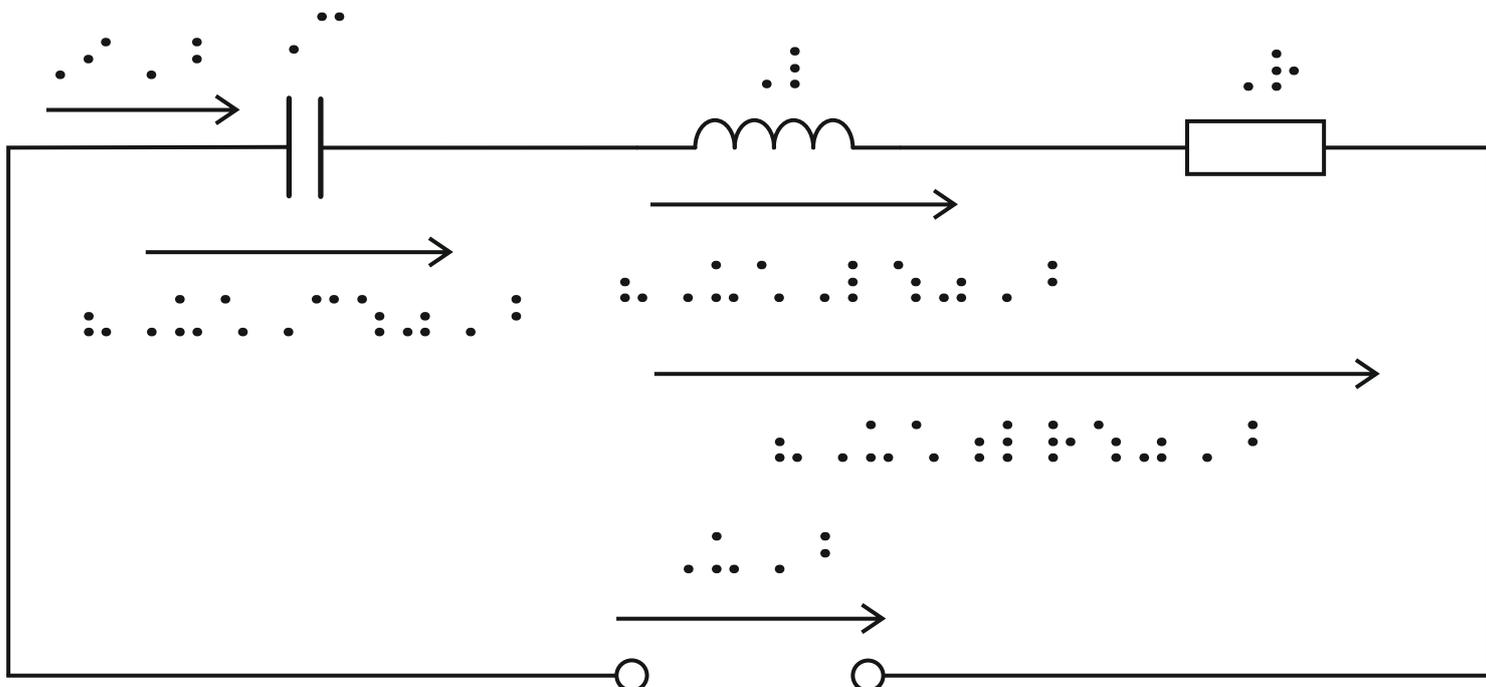


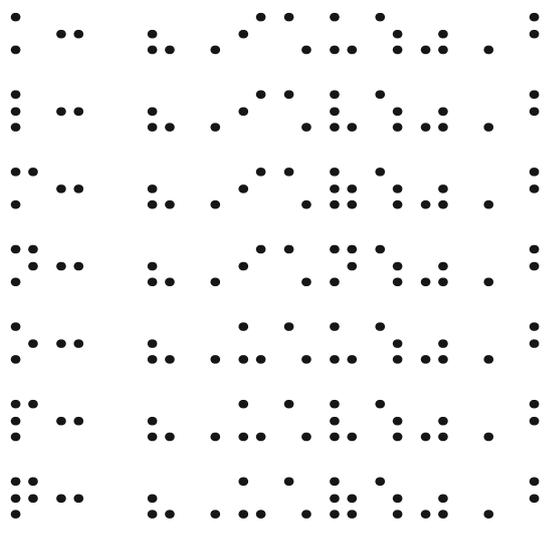
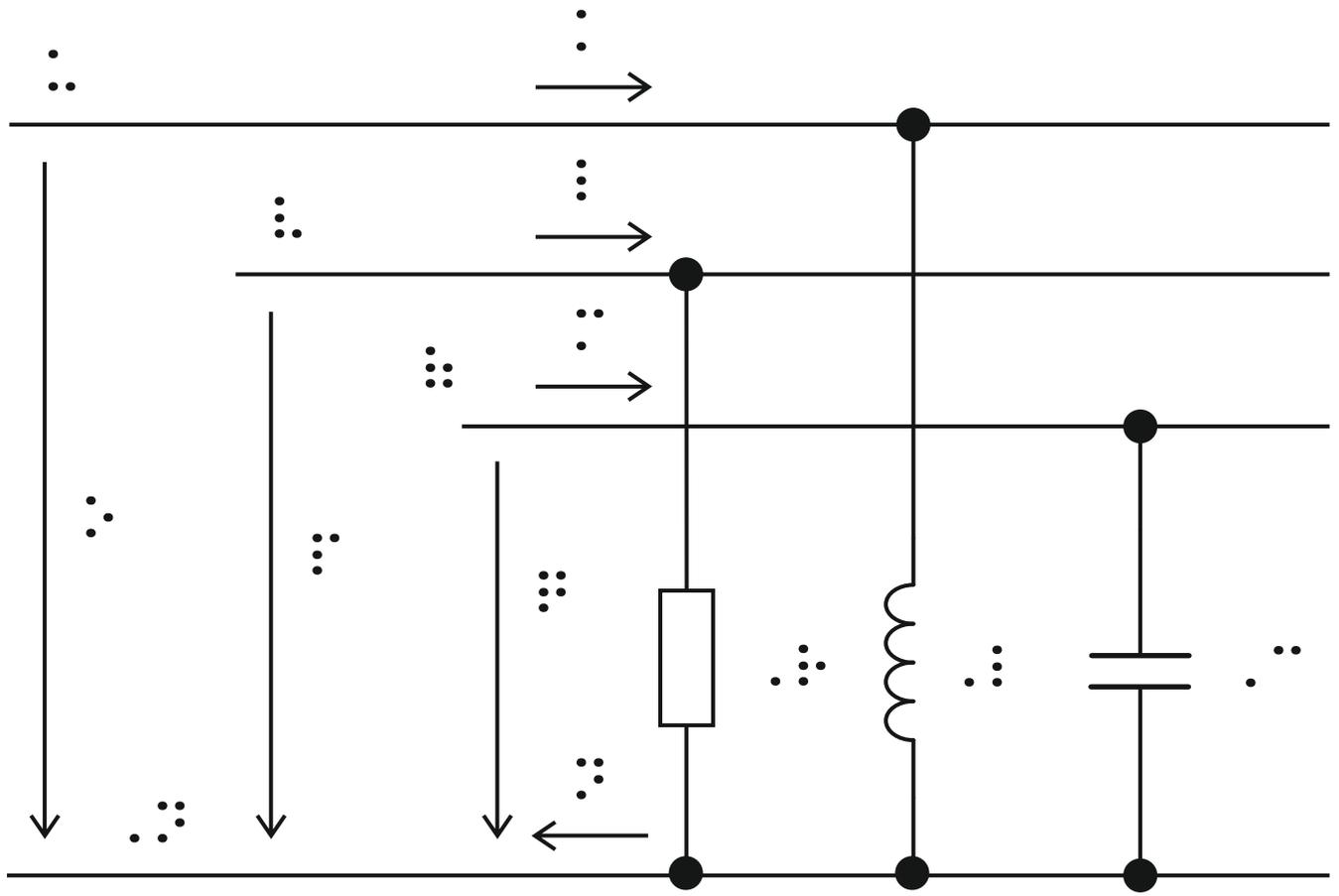
Braille characters arranged in a grid, likely representing labels for the circuit components and current directions.

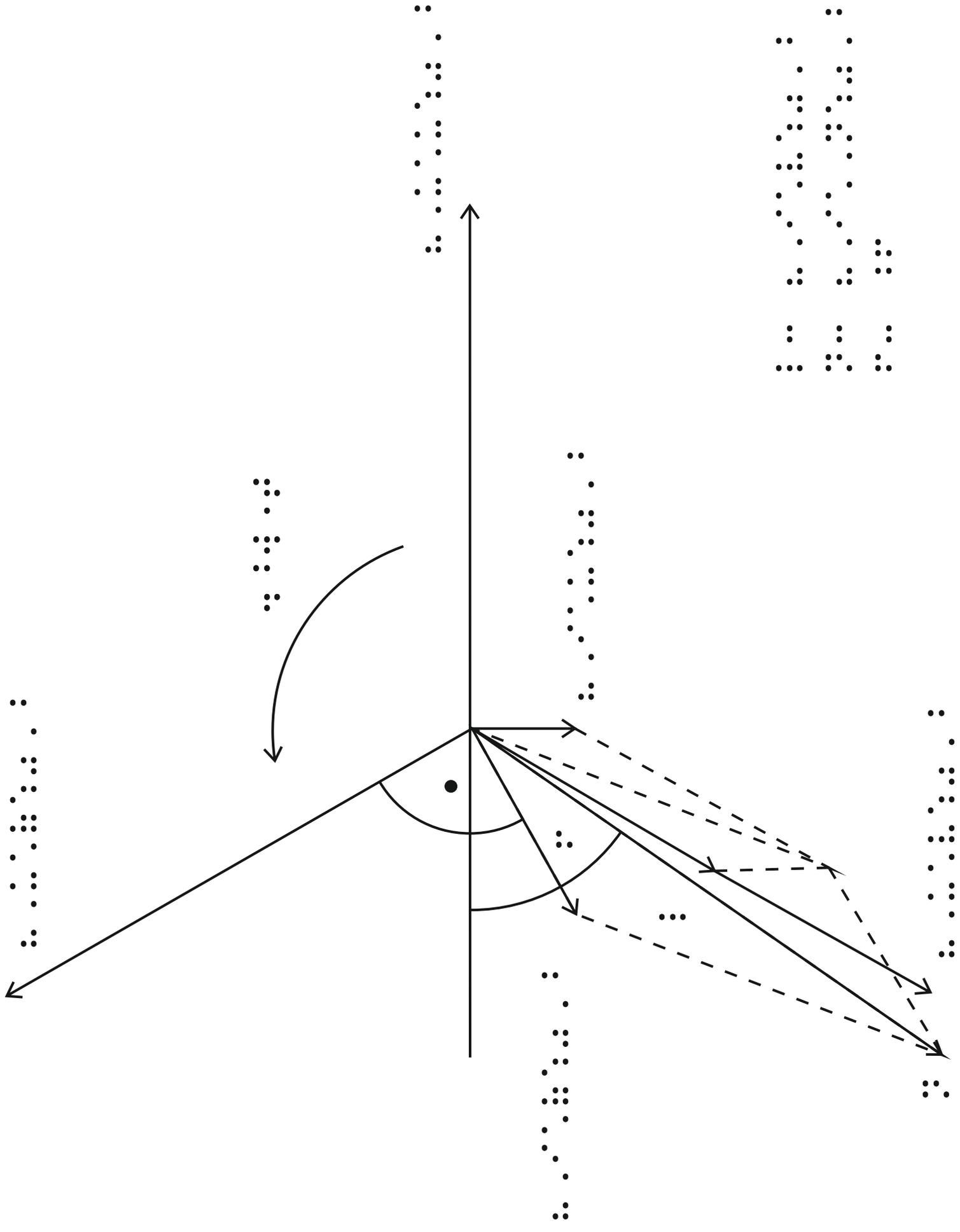


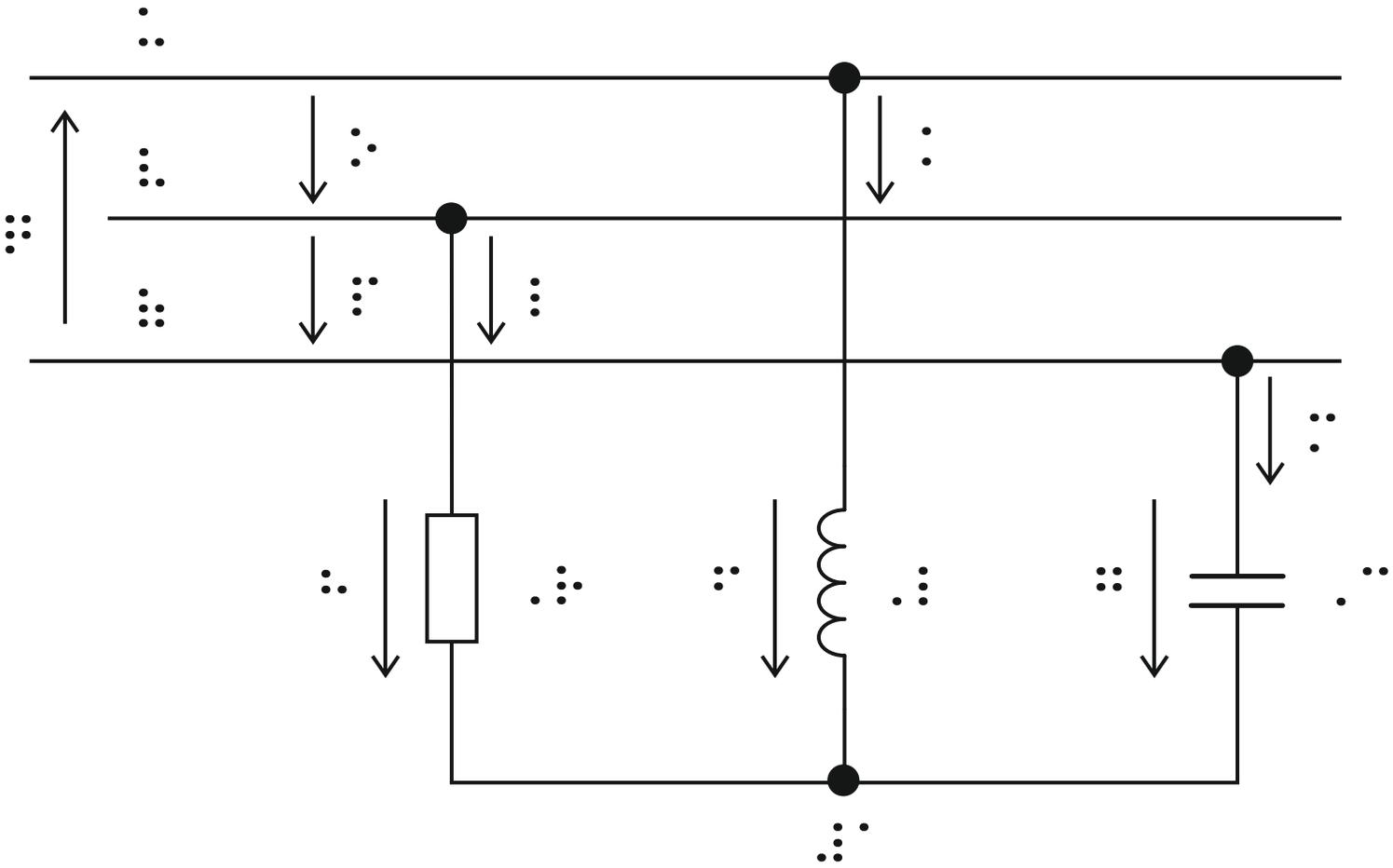


Braille text consisting of multiple lines of characters.





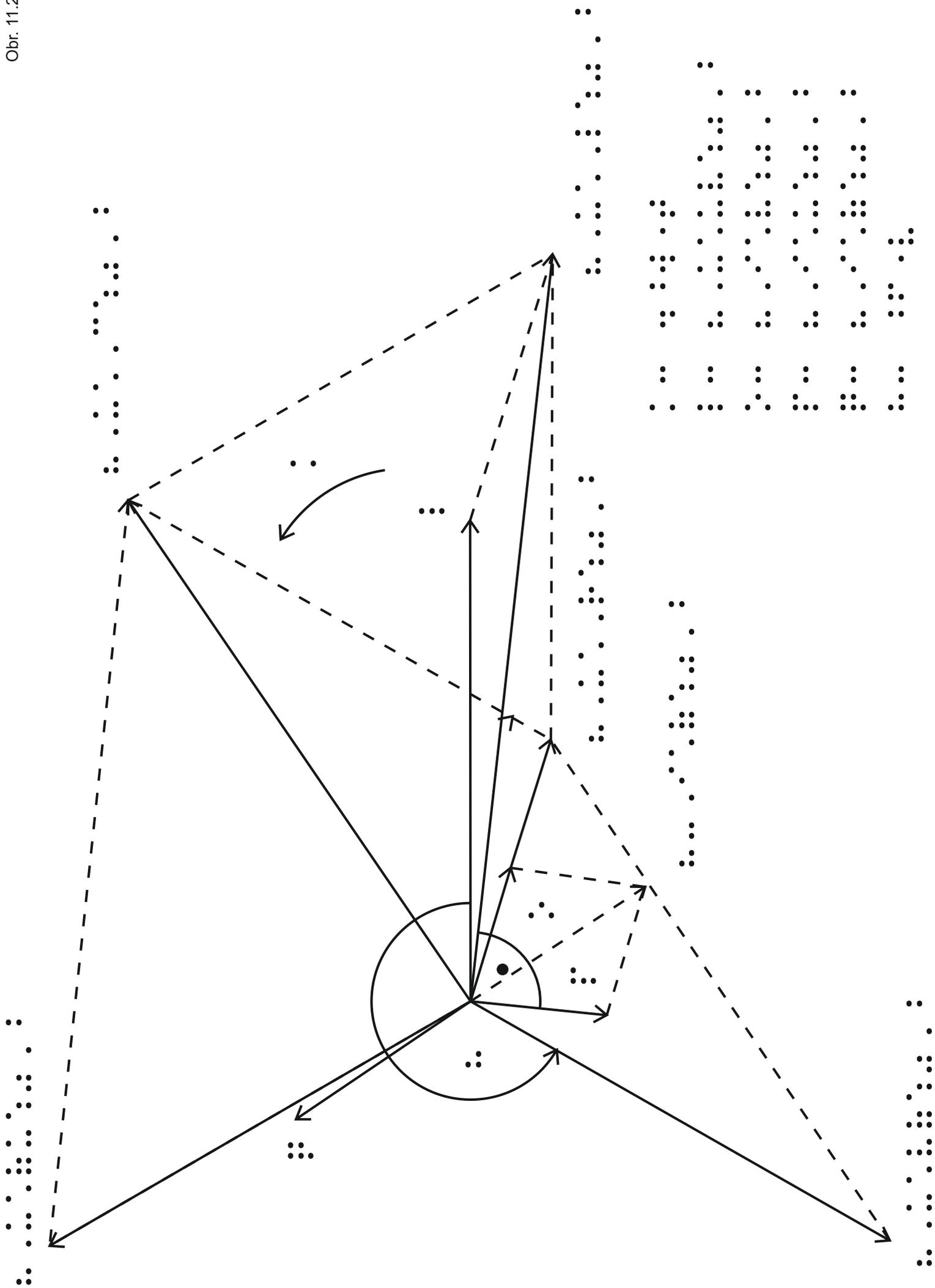


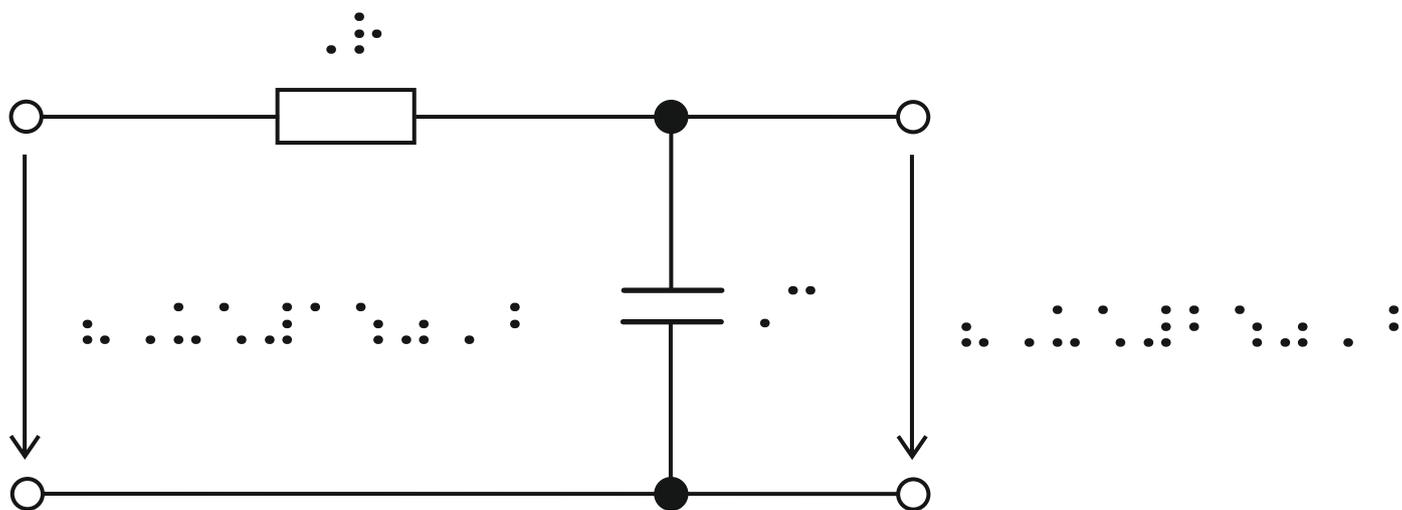
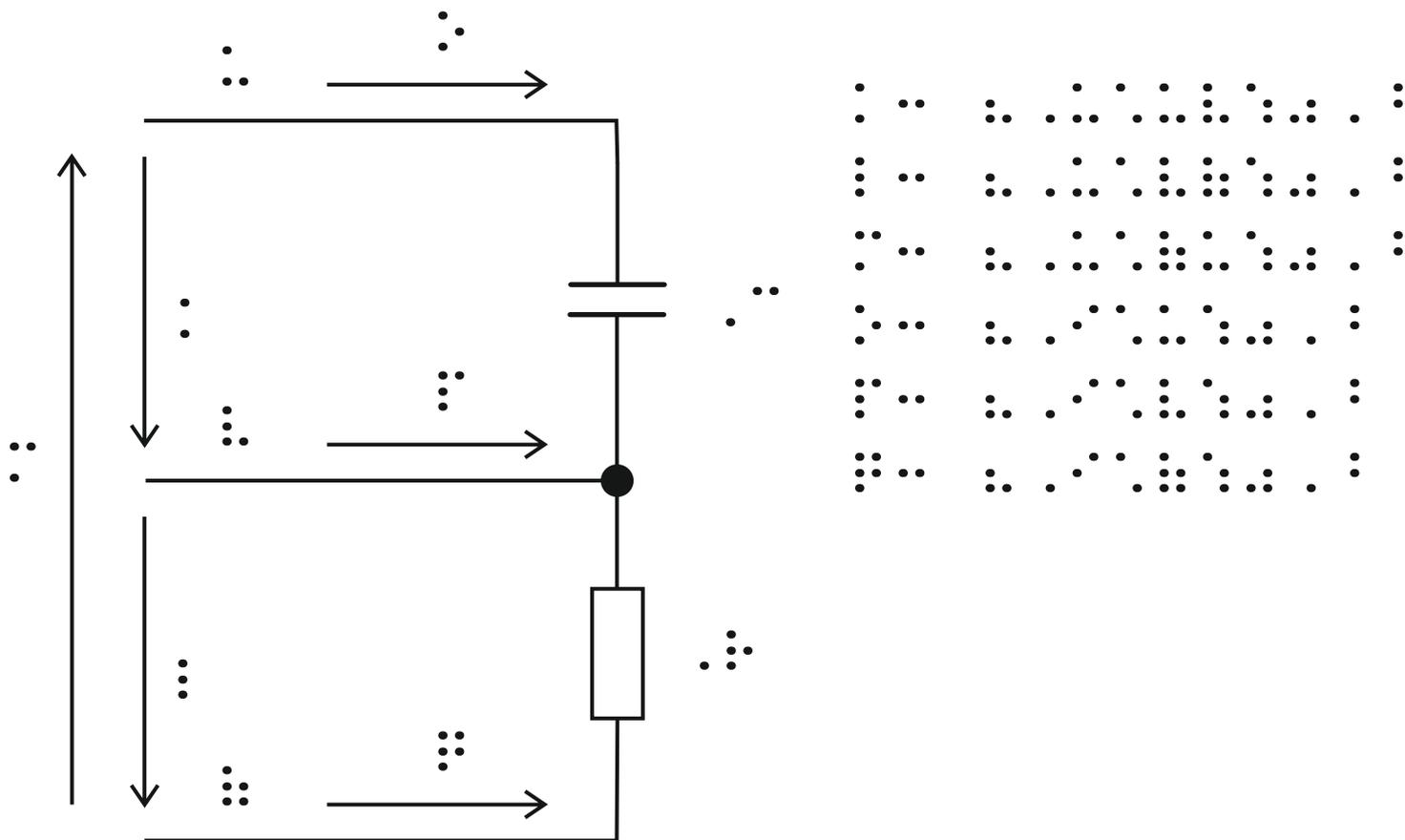


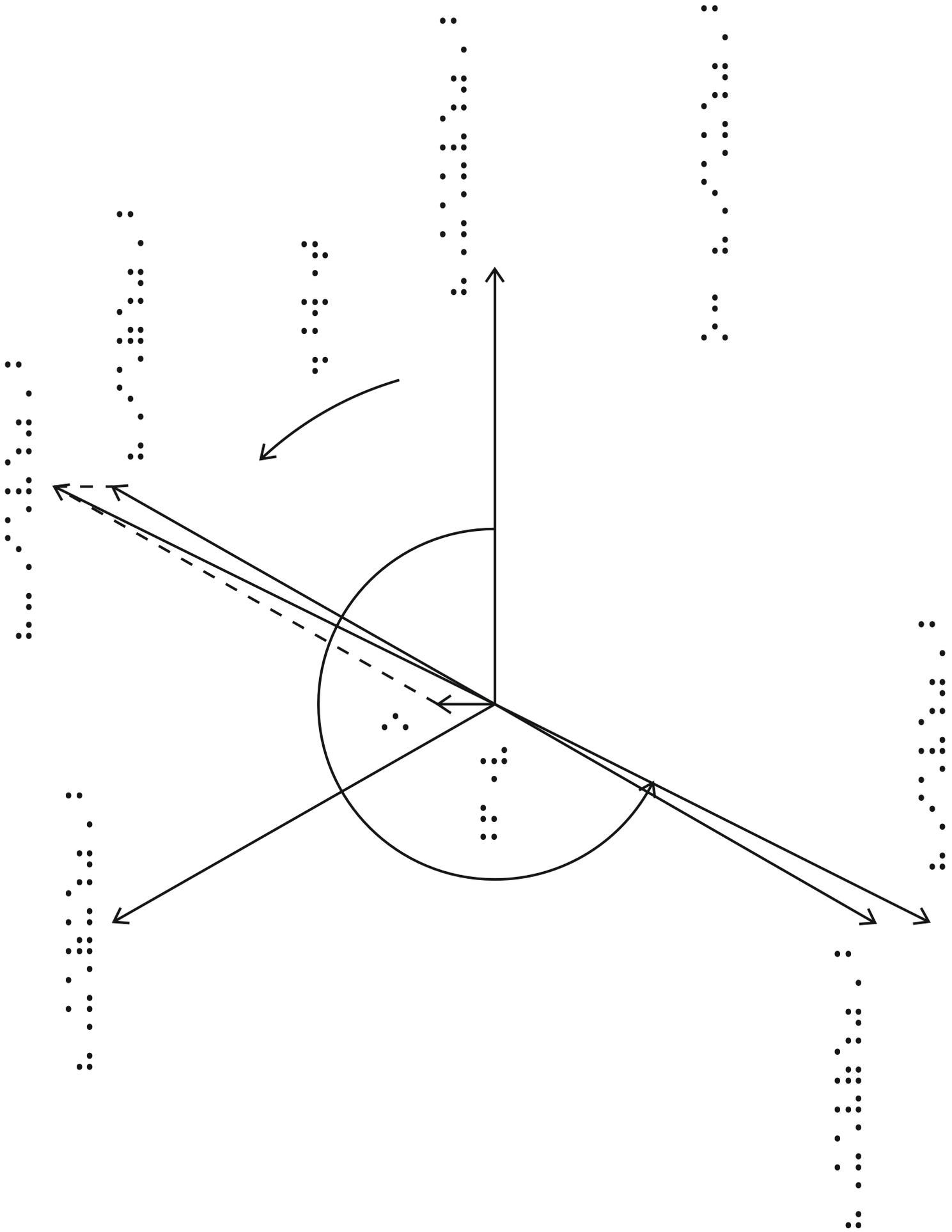
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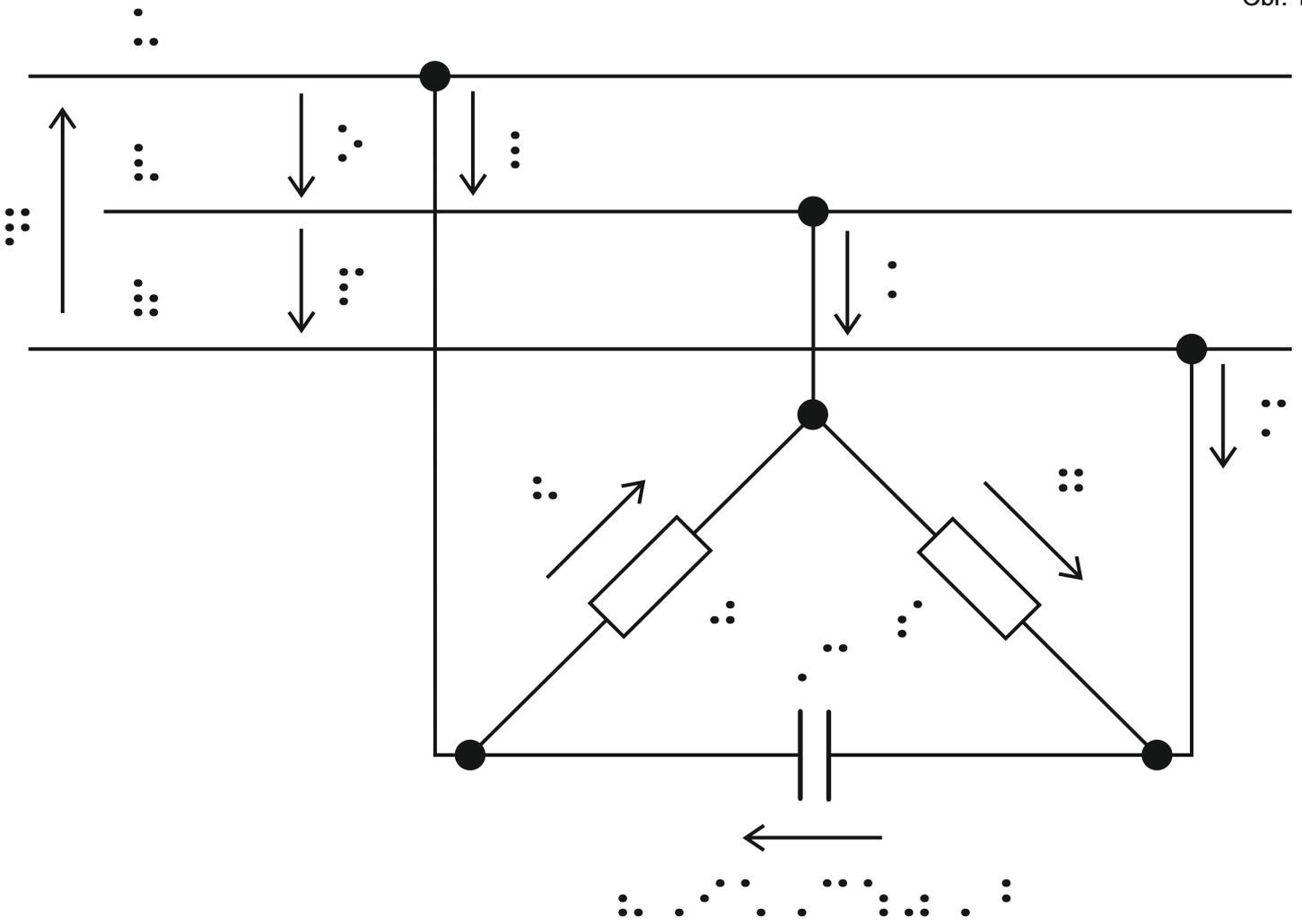
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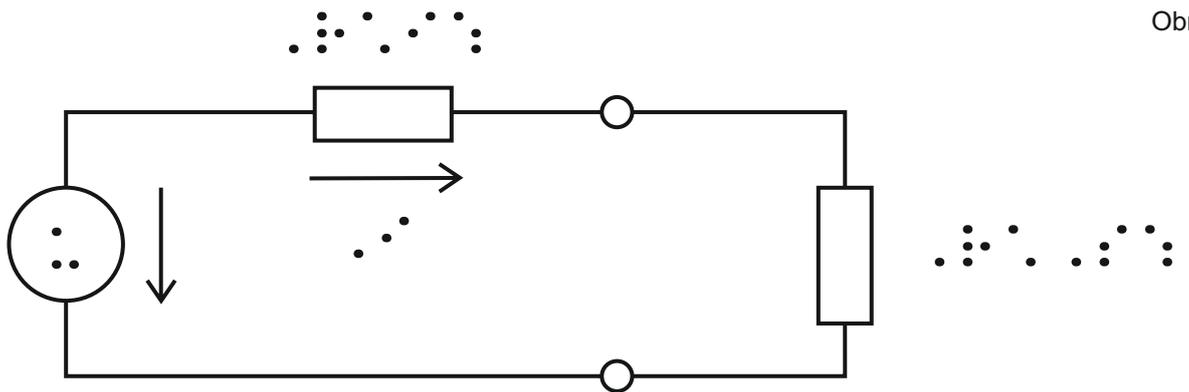




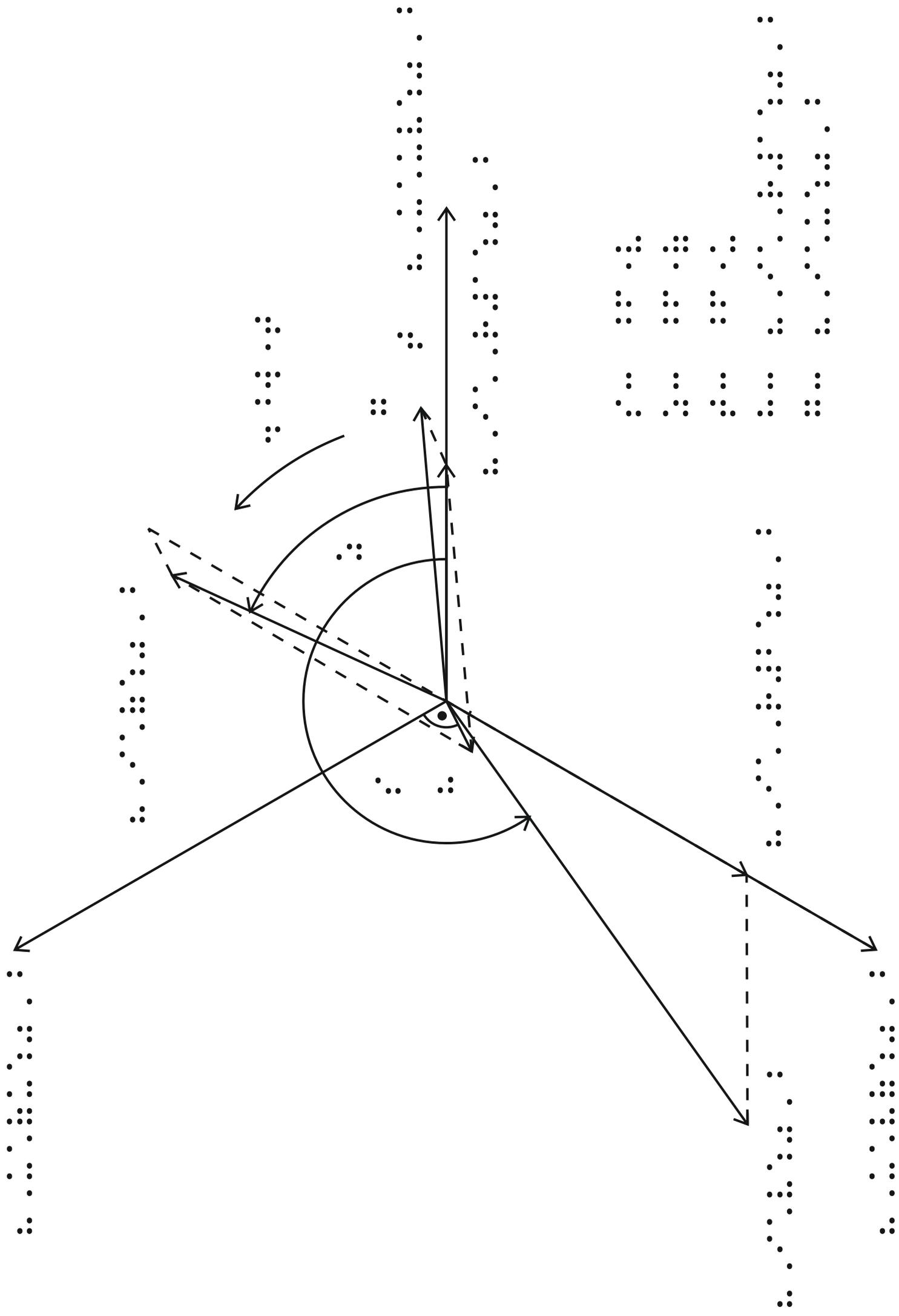


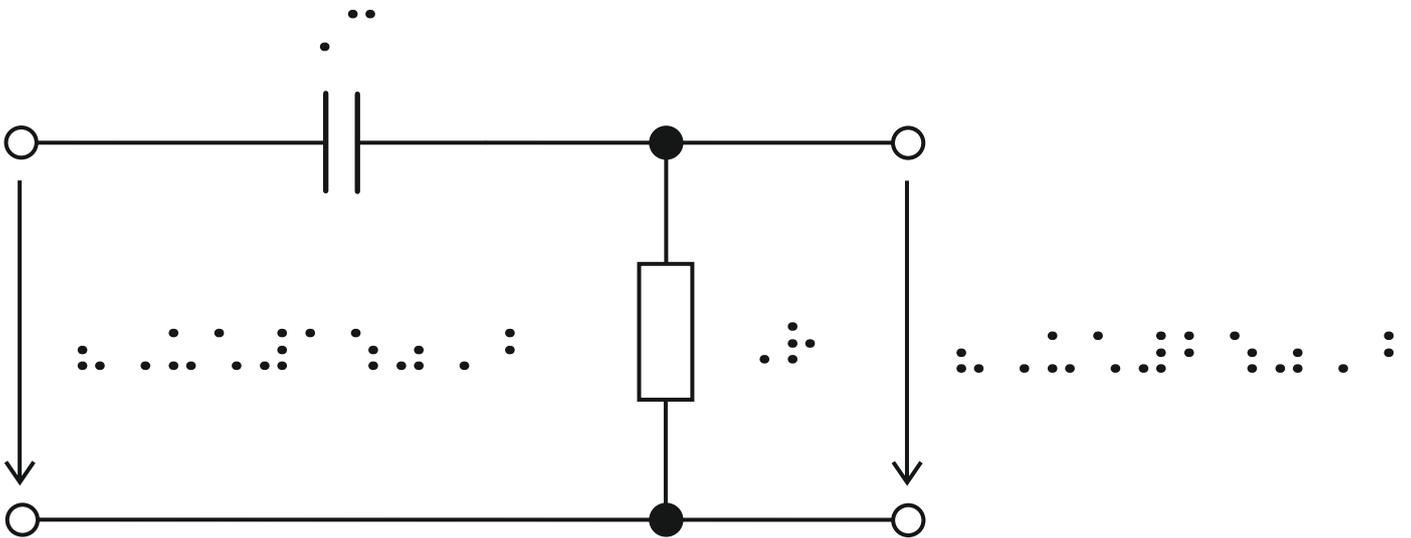
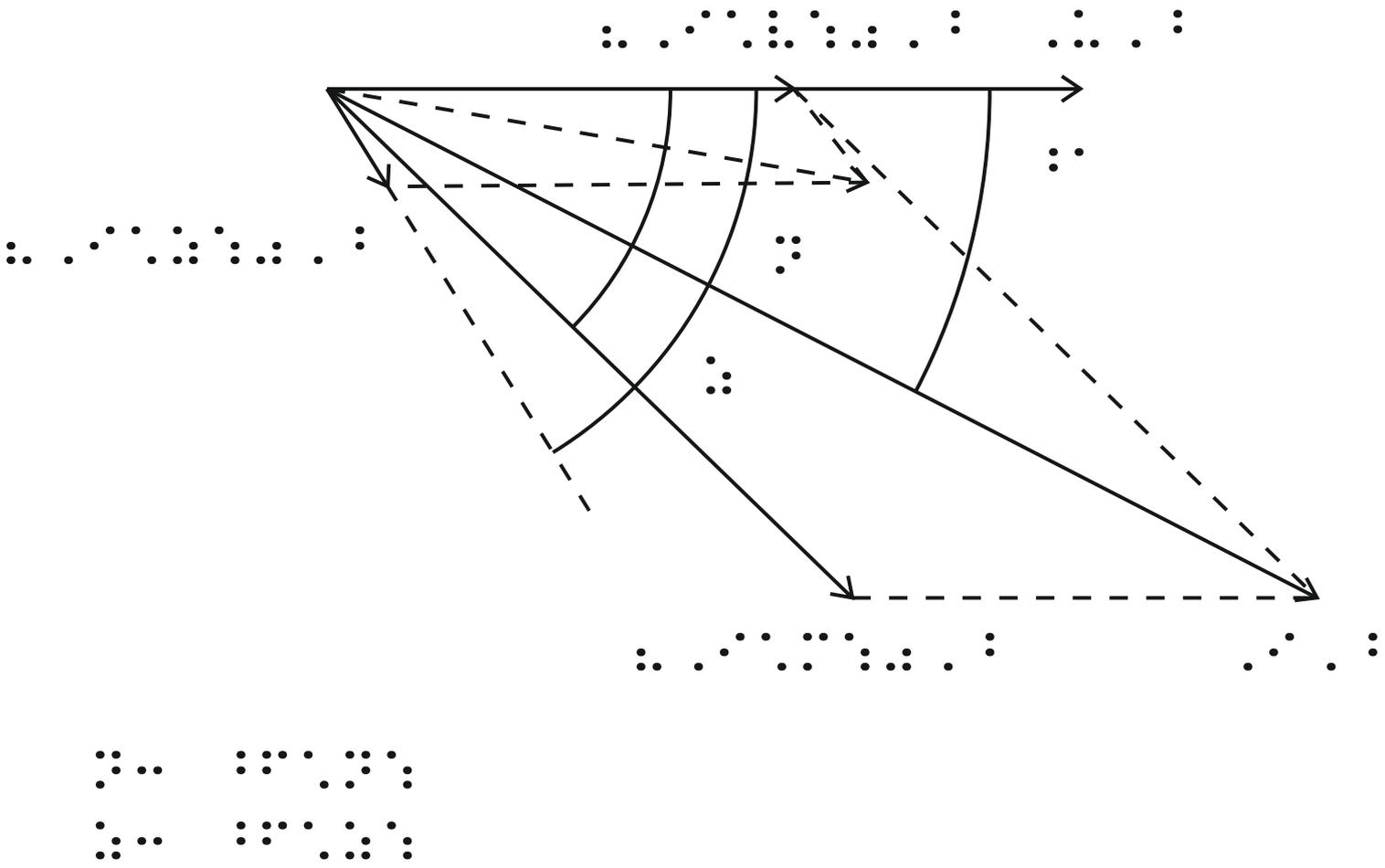
Braille text describing the circuit components and their connections, organized into two columns.

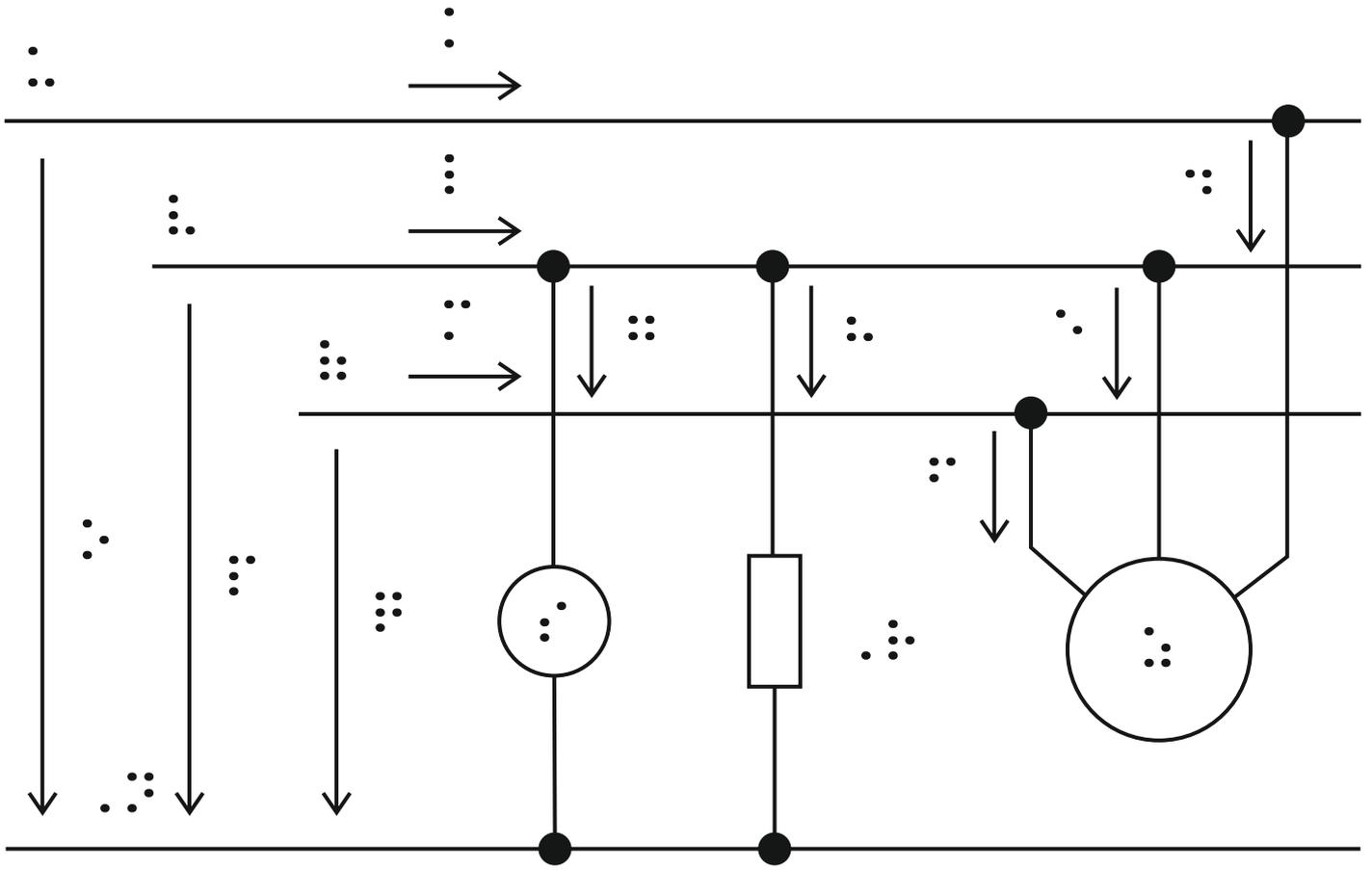
Obr. 12.1



Braille text located at the bottom left of the page, likely describing the circuit in Obr. 12.1.







Braille representation of the diagram above, consisting of several lines of Braille characters.

